

Prospective Data

Insulated Gate Bi-Polar Transistor

Type T0900DF65A

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{CES}	Collector – emitter voltage	6500	V
V_{CES}	Collector – emitter voltage (T_j 25°C)	6500	V
V_{CES}	Collector – emitter voltage (T_j -40°C)	6000	V
$V_{DC\ link}$	Permanent DC voltage for 100 FIT failure rate.	3600	V
V_{GES}	Peak gate – emitter voltage	±20	V

	RATINGS	MAXIMUM LIMITS	UNITS
I_C	DC collector current, IGBT	900	A
I_{CRM}	Repetitive peak collector current, $t_p=1ms$, IGBT	1800	A
$I_{F(DC)}$	Continuous DC forward current, Diode	900	A
I_{FRM}	Repetitive peak forward current, $t_p=1ms$, Diode	1800	A
I_{FSM}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}=60\%V_{RRM}$, Diode (Note 4)	7590	A
I_{FSM2}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}\leq 10V$, Diode (Note 4)	8350	A
P_{MAX}	Maximum power dissipation, IGBT (Note 2)	10.6	KW
$(di/dt)_{cr}$	Critical diode di/dt (note 3)	3000	A/ μs
T_j	Operating temperature range.	-40 to +125	°C
T_{stg}	Storage temperature range.	-40 to +125	°C

Notes: -

- 1) Unless otherwise indicated $T_j = 125^\circ C$.
- 2) $T_{sink} = 25^\circ C$, double side cooled.
- 3) Maximum commutation loop inductance 300nH.
- 4) Half-sinewave, 125°C T_j initial.

Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
$V_{CE(sat)}$	Collector – emitter saturation voltage	-	3.6	-	$I_C = 900A$, $V_{GE} = 15V$, $T_j = 25^\circ C$	V
		4.4	4.8	5.2	$I_C = 900A$, $V_{GE} = 15V$	V
V_{T0}	Threshold voltage	-	-	2.49	Current range: 300A – 900A	V
r_T	Slope resistance	-	-	3.02		mΩ
$V_{GE(TH)}$	Gate threshold voltage	-	5.2	-	$V_{CE} = V_{GE}$, $I_C = 900mA$	V
I_{CES}	Collector – emitter cut-off current	-	10	35	$V_{CE} = V_{CES}$, $V_{GE} = 0V$	mA
I_{GES}	Gate leakage current	-	-	40	$V_{GE} = \pm 20V$	μA
C_{ies}	Input capacitance	-	160	-	$V_{CE} = 10V$, $V_{GE} = 0V$, $f = 100kHz$, $T_j = 25^\circ C$	nF
$t_{d(on)}$	Turn-on delay time	-	2.1	-	$I_C = 900A$, $V_{CE} = 3600V$, $di/dt = 2500A/\mu s$ $V_{GE} = \pm 15V$, $L_s = 300nH$ $R_{g(ON)} = 3.3\Omega$, $R_{g(OFF)} = 11\Omega$, $C_{GE} = 68nF$ Integral diode used as freewheel diode (Note 3 & 4)	μs
$t_r(V)$	Rise time	-	2.5	-		μs
$Q_{g(on)}$	Turn-on gate charge	-	5	-		μC
E_{on}	Turn-on energy	-	6.3	-		J
$t_{d(off)}$	Turn-off delay time	-	4.3	-		μs
$t_f(l)$	Fall time	-	2.3	-		μs
$Q_{g(off)}$	Turn-off gate charge	-	5.5	-		μC
E_{off}	Turn-off energy	-	5.1	-		J
I_{SC}	Short circuit current	-	4900	-	$V_{GE} = +15V$, $V_{CC} = 3600V$, $V_{CEmax} \leq V_{CES}$, $t_p \leq 10\mu s$	A

Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V_F	Forward voltage	-	3	-	$I_F = 900A$, $T_j = 25^\circ C$	V
		-	3.4	3.8	$I_F = 900A$	V
V_{T0}	Threshold voltage	-	-	1.89	Current range 300A – 900A	V
r_T	Slope resistance	-	-	2.12		mΩ
I_{rm}	Peak reverse recovery current	-	950	-	$I_F = 900A$, $V_{GE} = -15V$, $di/dt = 2500A/\mu s$	A
Q_{rr}	Recovered charge	-	1500	-		μC
t_{rr}	Reverse recovery time, 50% chord	-	1.2	-		μs
E_r	Reverse recovery energy	-	2.2	-		J

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
R_{thJK}	Thermal resistance junction to sink, IGBT	-	-	9.4	Double side cooled	K/kW
		-	-	14.3	Collector side cooled	K/kW
		-	-	27.6	Emitter side cooled	K/kW
R_{thJK}	Thermal resistance junction to sink, Diode	-	-	16	Double side cooled	K/kW
		-	-	23.4	Cathode side cooled	K/kW
		-	-	50.6	Anode side cooled	K/kW
F	Mounting force	45	-	55	Note 2	kN
W_t	Weight	-	2.2	-		g

Notes:-

- 1) Unless otherwise indicated $T_j = 125^\circ C$.
- 2) Consult application note 2008AN01 for detailed mounting requirements
- 3) C_{GE} is additional gate – emitter capacitance added to output of gate drive
- 4) Figures 6 to 9 are obtained using integral diode as freewheeling diode

Curves

Figure 1 – Typical collector-emitter saturation voltage characteristics

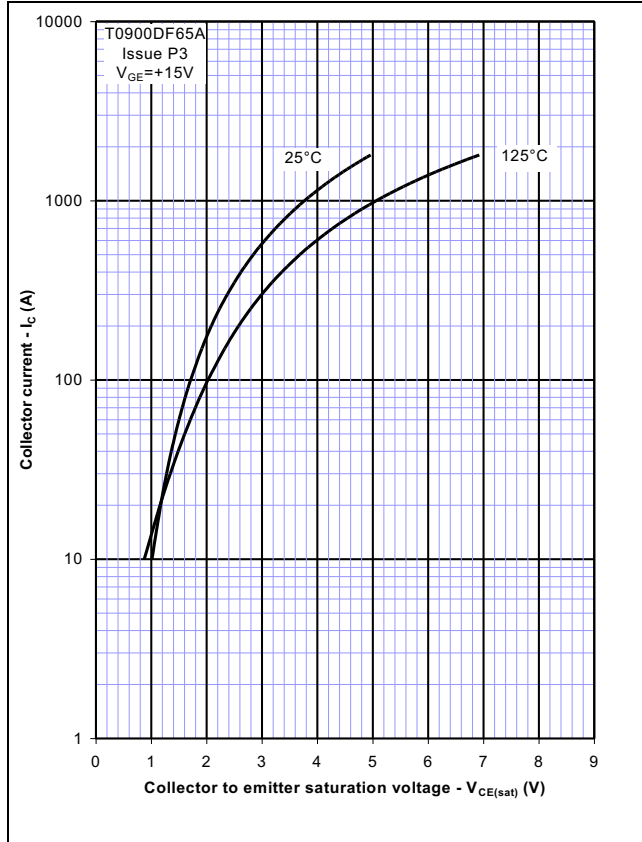


Figure 2 – Typical output characteristic

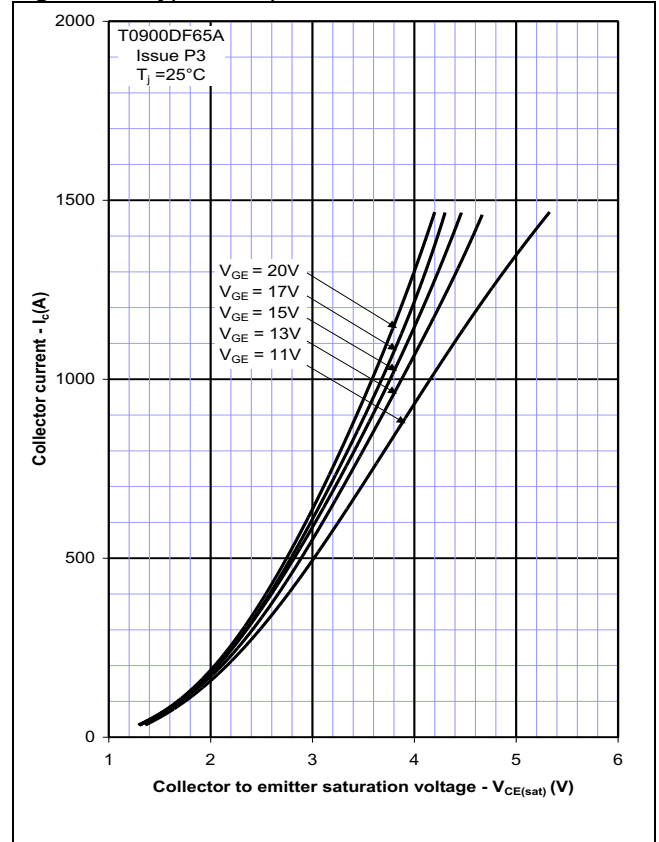


Figure 3 – Typical output characteristic

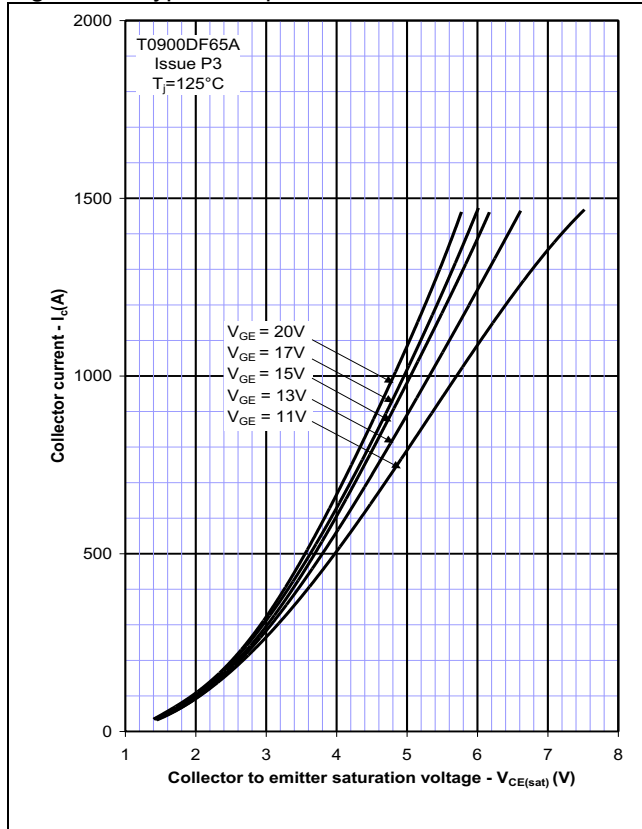


Figure 4 – Typical turn-on delay time vs gate resistance

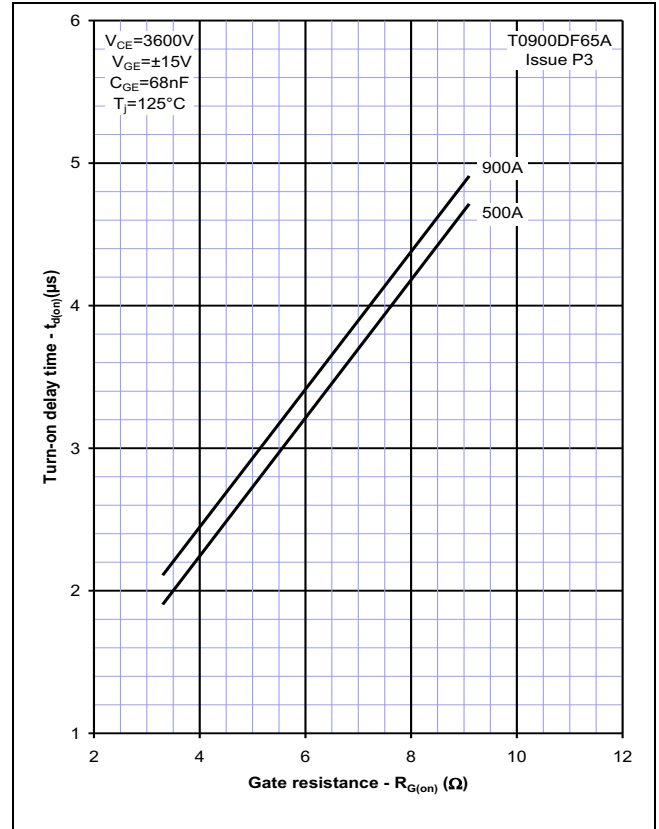


Figure 5 – Typical turn-off delay time vs. gate resistance

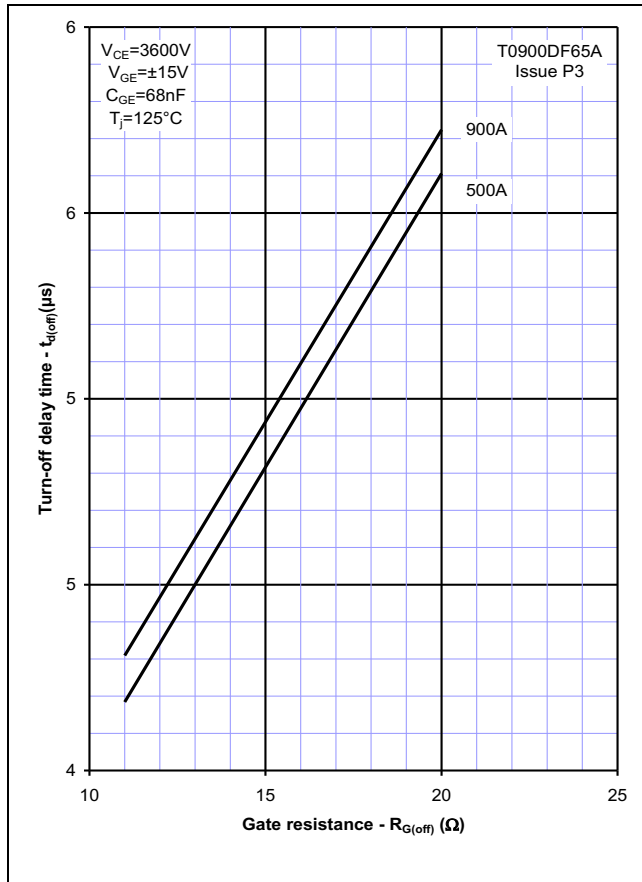


Figure 6 – Typical turn-on energy vs. collector current

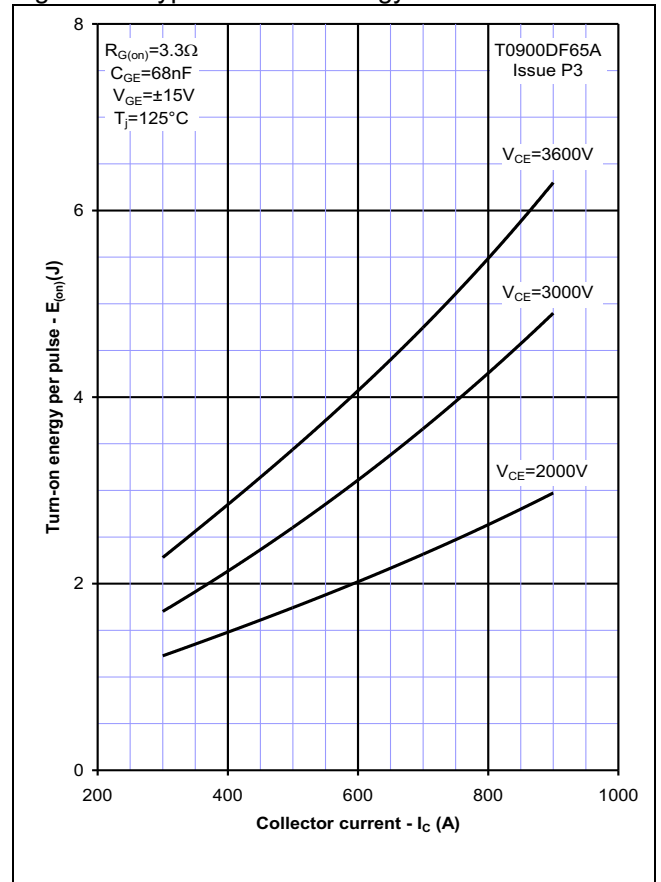


Figure 7 – Typical turn-on energy vs. di/dt

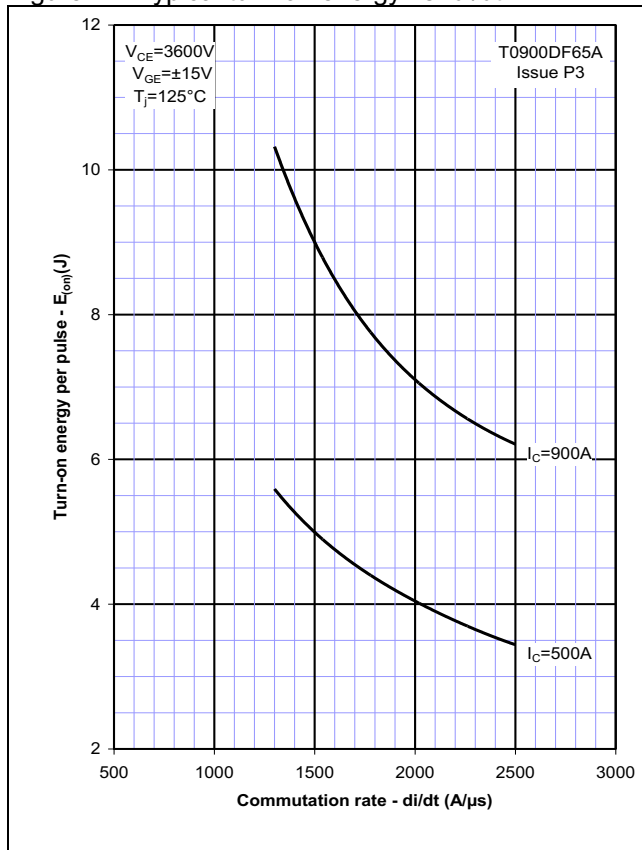


Figure 8 – Typical turn-off energy vs. collector current

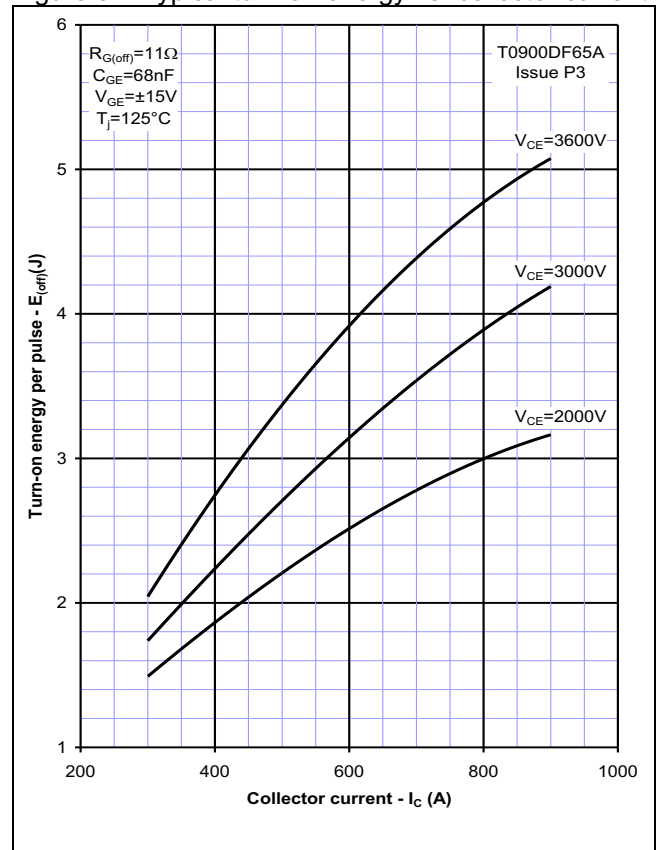


Figure 9 – Turn-off energy vs voltage

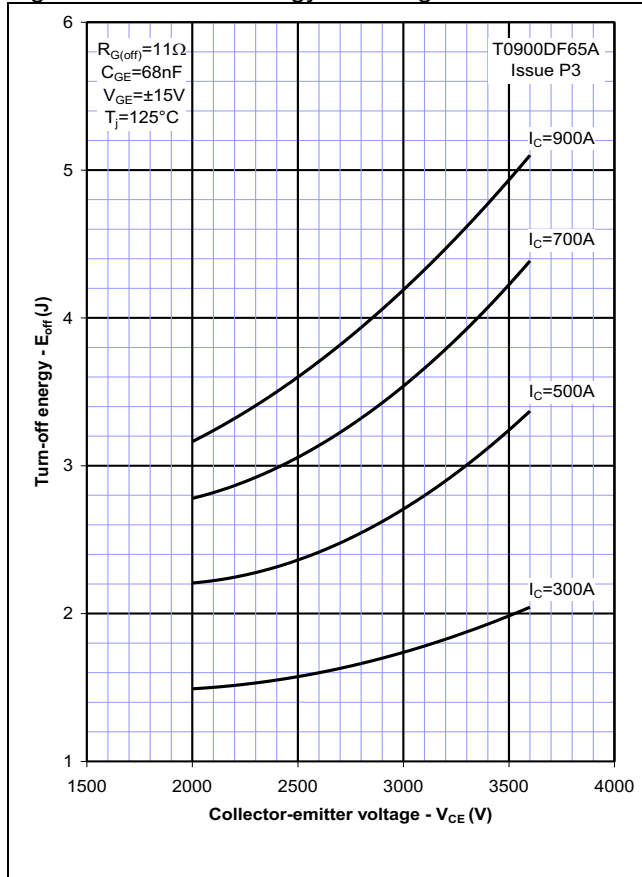


Figure 10 – Safe operating area (IGBT)

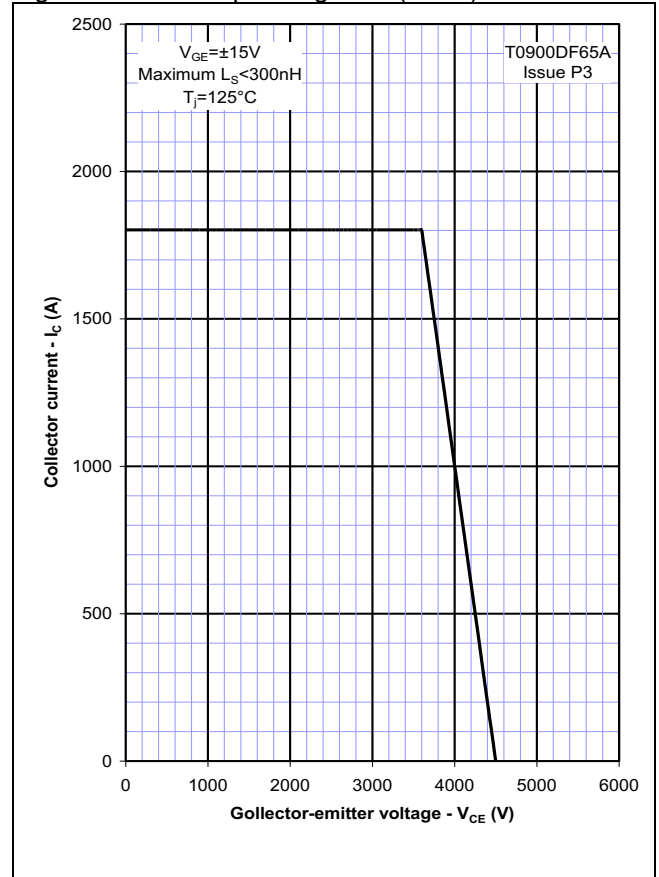


Figure 11 – Typical diode forward characteristics

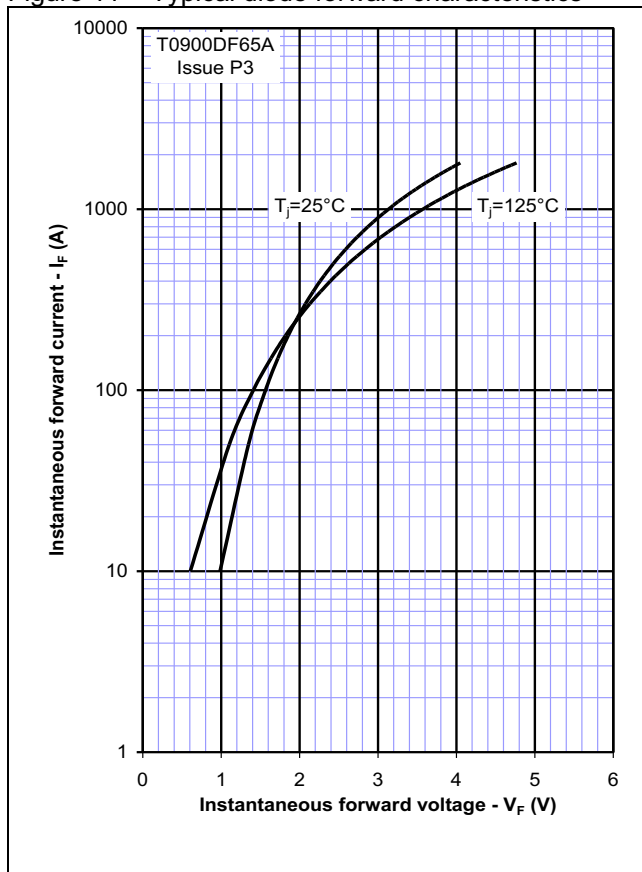


Figure 12 – Typical recovered charge

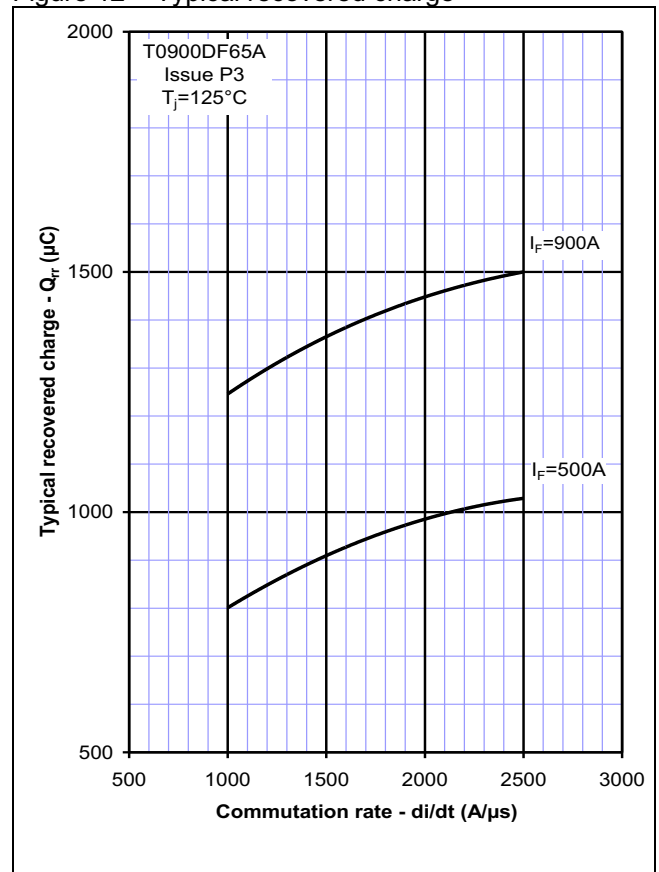


Figure 13 – Typical reverse recovery current

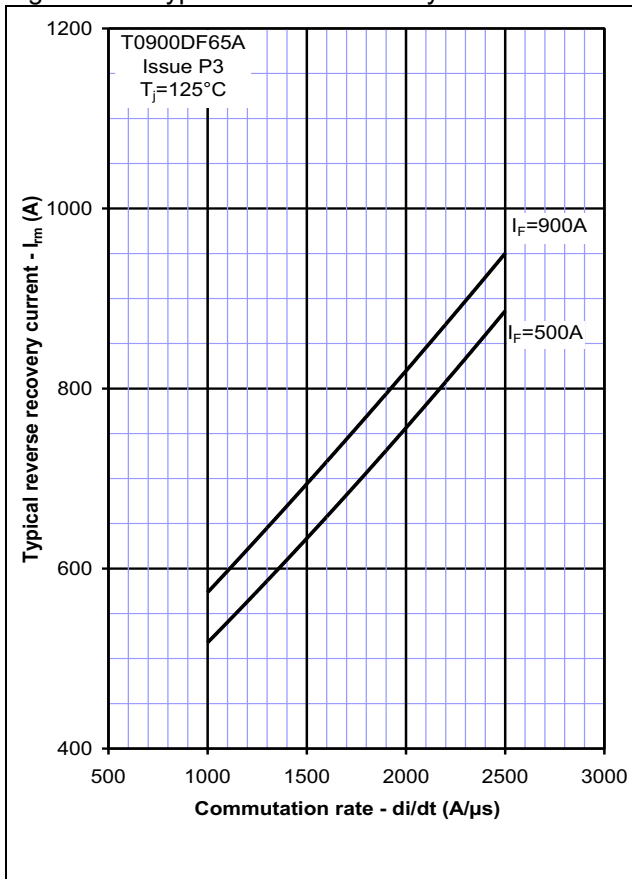


Figure 14 – Typical reverse recovery time

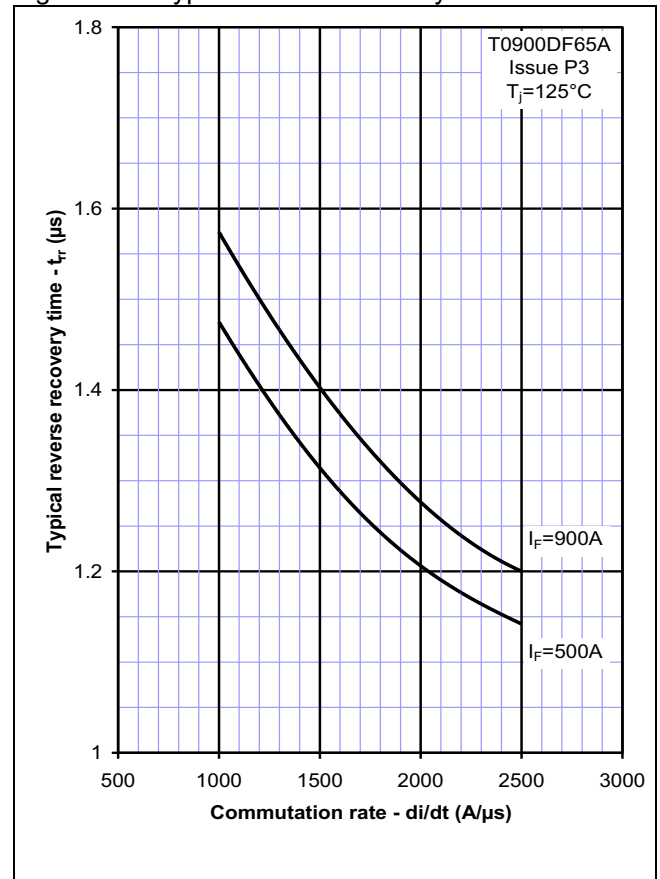


Figure 15 – Typical reverse recovery energy

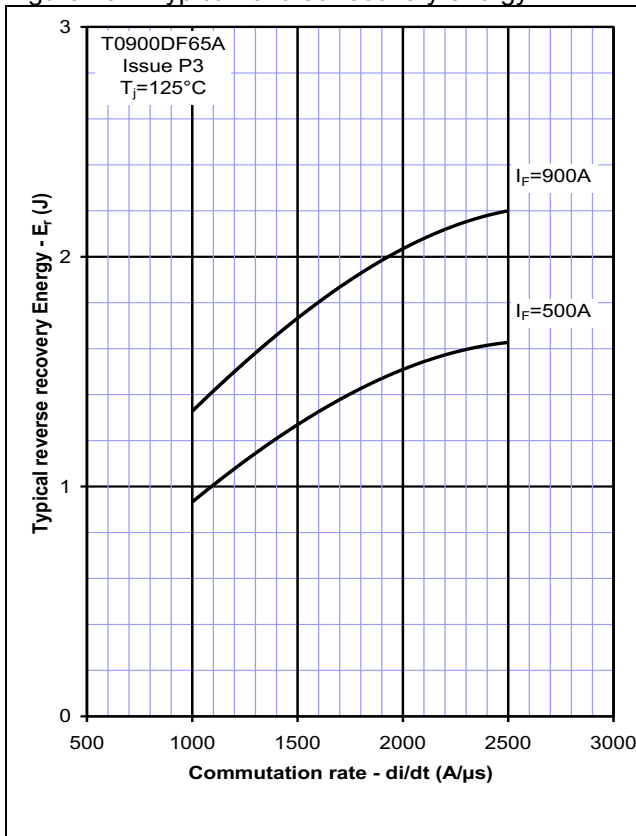


Figure 16 – Safe operating area (Diode)

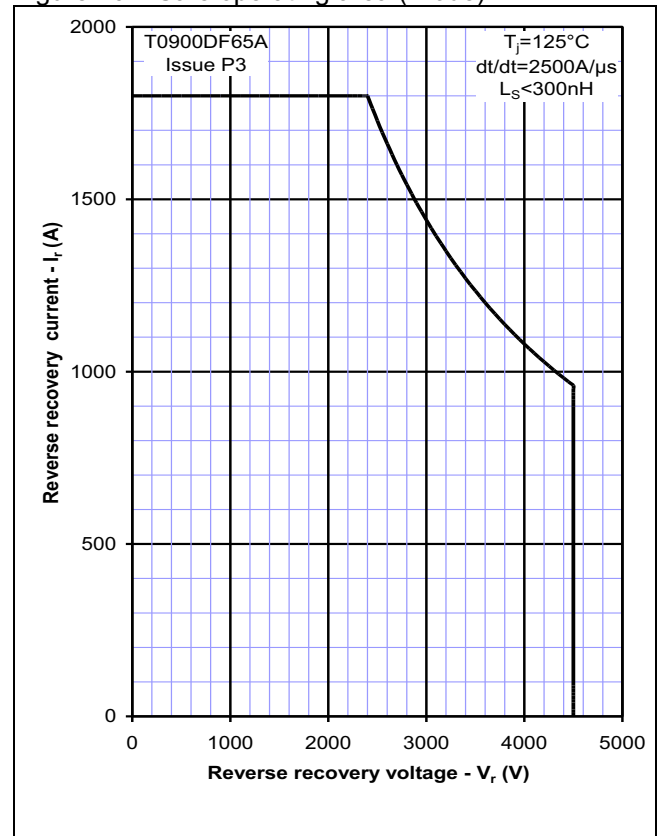


Figure 17 – Transient thermal impedance (IGBT)

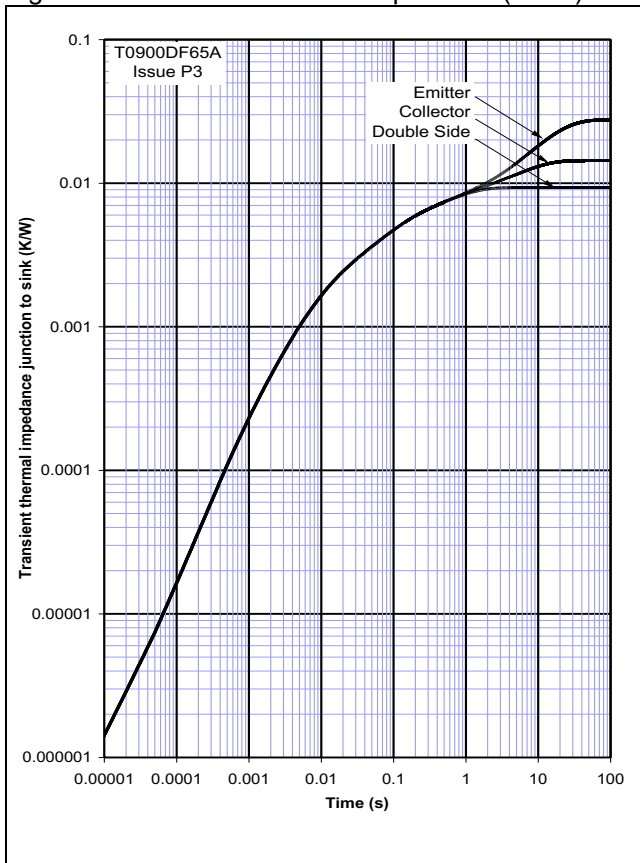
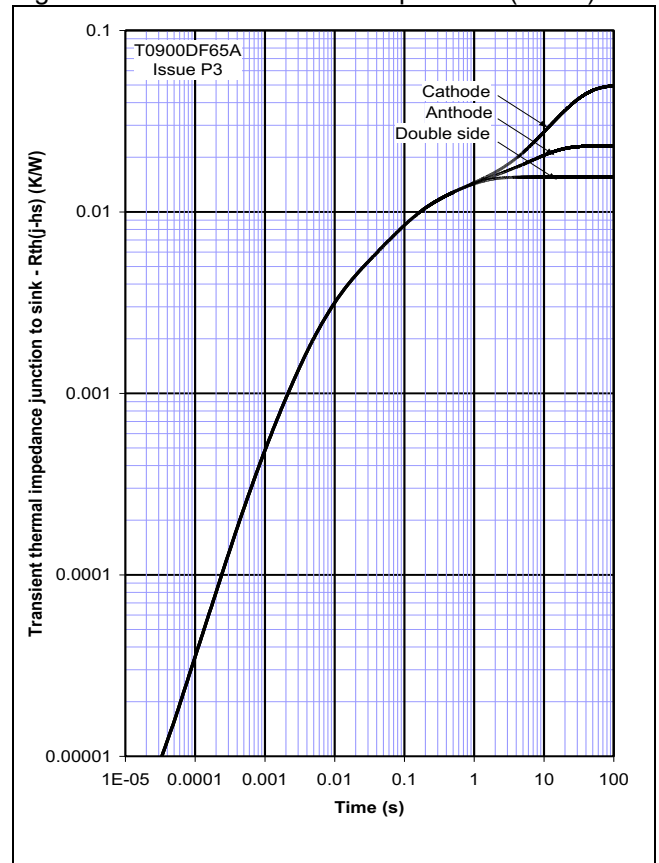
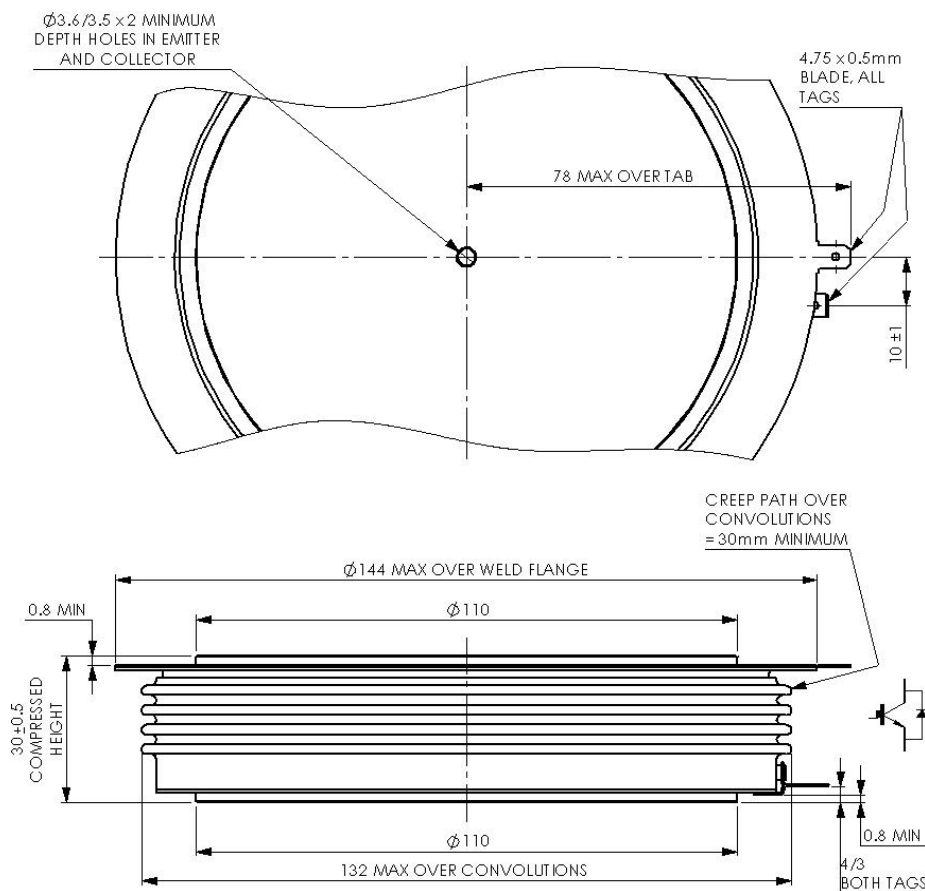


Figure 18 – Transient thermal impedance (Diode)



Outline Drawing & Ordering Information



101A409

ORDERING INFORMATION

(Please quote 10 digit code as below)

T0900	DF	65	A
Fixed type Code	Fixed Outline Code	Voltage Grade V _{CES} /100 65	Fixed format code

Typical order code: T0900DF65A ($V_{CES} = 6500V$)

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