

# RoHS Compliant Serial ATA Flash Drive

Specifications for SAFD25M2

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Version 1.4



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## **Features:**

- Standard Serial ATA 2.5 (Gen. 2)
  - Serial ATA 2.5 (Gen. 2)
  - SATA II, 3.0 Gbps
  - ATA-compatible command set
- Capacities

•

- 4, 8, 16, and 32 GB
- Performance
  - Burst read/write: 300 MB/sec
  - Sustained read: up to 100 MB/sec
  - Sustained write: up to 100 MB/sec

#### • Intelligent endurance design

- Built-in hardware BCH ECC, correcting 8-bit or 15-bit error per 512-byte data sector
- Static wear-leveling scheme together with dynamical block allocation to significantly increase the lifetime of a flash device and optimize the disk performance
- Flash bad-block management
- S.M.A.R.T. technology
- Power Failure Management
- Quick Erase
- NAND Flash Type: SLC

- Data integrity under power-cycling – No battery required for data storage
- Temperature
  - 0°C to 70°C for operating
  - -40°C to 100°C for storage
- Supply voltage
  - 5.0 V ± 5%
- Low power consumption
  - Active mode: 260 mA (5.0 V)
  - Idle mode: 130 mA (5.0 V)
- Form factor
  - 2.5 inch
- Connector
  - 7-pin SATA male connector
  - 15-pin SATA power connector
- RoHS compliant

## Serial ATA Flash Drive AP-SAFD25MPAxxxS-E



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## **1. Product Description**

#### 1.1 Introduction

Apacer's Serial ATA Flash Drive (SAFD25M2) is a solid-state disk (SSD) drive that contains a controller, embedded firmware, and flash media along with a male connector. Using NAND flash memory devices, the SAFD25M2 drive interfaces with the host allowing data to be seamlessly transferred between the host and the flash devices.

The SAFD25M2 drive is designed with a single-chip controller, offering capacities of up to 32 gigabytes and providing full support for the SATA II high-speed interface standard. It can operate at sustained access rates of up to 100 megabytes per second, which is much faster than any other solid-state or traditional SATA drive currently available on the market.

In addition to buffer management through dynamical allocation, the SAFD25M2 adopts the static wear-leveling scheme to allow uniform use of all storage blocks, ensuring that the lifetime of a flash media can be significantly increased and the disk performance is optimized as well. The SAFD25M2 provides the S.M.A.R.T. feature complies to the SATA Rev.2.6, ATA/ATAPI-7 specifications and uses the standard SMART command B0h to read data from the drive. This feature protects the user from unscheduled downtime by monitoring and storing critical drive performance.

## **1.2 Functional Block Diagram**

The SAFD25M2 drive includes a single-chip SATA II Controller and the flash media, as well as the SATA standard interface. The controller integrates the flash management unit with the controller itself to support multi-channel, multi-bank flash arrays. Figure 1-1 shows the functional block diagram.

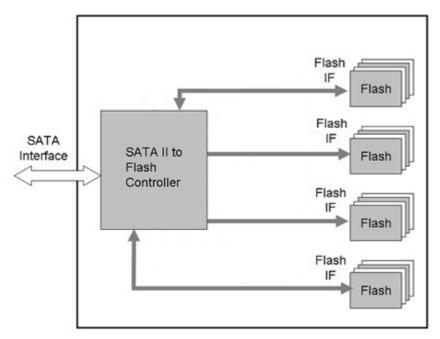


Figure 1-1 Apacer SAFD25M2 block diagram



#### **1.3 ATA Mode Support**

The SAFD25M2 provides ATA mode support as follows:

- Up to PIO mode-4
- Up to Multiword DMA mode-2
- Up to UDMA mode-5

## **1.4 Capacity Specification**

Capacity specification of the SAFD product family is available as shown in Table 1-1. It lists the specific capacity, the default numbers of logical cylinders and heads, and the number of logical sectors per track for each product line.

Capacity	Total Bytes	Cylinders	Heads	Sectors	Max LBA
4 GB	4,021,936,128	7793	16	63	7,855,344
8 GB	8,061,419,520	15620	16	63	15,744,960
16 GB	16,139,681,792	16383*	16	63	31,522,8161
32 GB	32,296,140,800	16383*	16	63	63,078,4001

Table 1-1 Capacity specification

\*Cylinders, heads or sectors are not applicable for these capacities. Only LBA addressing applies.

#### 1.5 Performance

Performance of the SAFD25M2 is shown in Table 1-2.

#### Table 1-2 Performance specification

Capacity Performance	4 GB	8 GB	16 GB	32 GB
Sustained read (MB/s)	102	102	102	103
Sustained write (MB/s)	55	57	99	99



## 1.5 Pin Assignments

Figure 1-2 illustrates pin assignments of Apacer SAFD25M2 pins, and Table1-3 and Table 1-4 describe its signal segment and power segment, respectively.

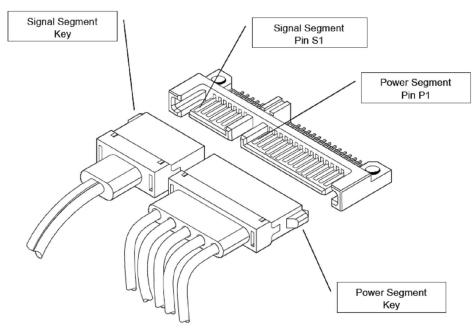


Figure 1-2 Apacer SAFD25M2 pin assignments

Name	Туре	Description
S1	GND	
S2	RxP	+ Differential Receive Signal
S3	RxN	- Differential Receive Signal
S4	GND	
S5	TxN	- Differential Transmit Signal
S6	TxP	+Differential Transmit Signal
S7	GND	

#### Table 1-3: Signal segment

#### Table 1-4: Power segment

Pin	Signal/Description
P1	Not used (3.3V)
P2	Not used (3.3V)
P3	Not used (3.3V)
P4	Ground
P5	Ground
P6	Ground
P7	5V
P8	5V
P9	5V
P10	Ground
P11	Ground
P12	Ground
P13	Not used (12V)
P14	Not used (12V)
P15	Not used (12V)



## **2. Software Interface**

### 2.1 Command Set

Table 2-1 summarizes the ATA commands supported by the SAFD25M2.

Command	Code	<b>FR</b> <sup>1</sup>	SC <sup>2</sup>	SN <sup>3</sup>	CY⁴	DH⁵	LBA <sup>6</sup>
Check-Power-Mode	E5H	-	_	_	_	D <sup>8</sup>	_
Execute-Drive-Diagnostic	90H	-	-	-	-	D	-
Flush-Cache	E7H	-	-	-	-	D	-
Identify-Drive	ECH	_	_	-	-	D	-
Idle	E3H	-	Y	-	-	D	-
Idle-Immediate	E1H	-	-	-	-	D	-
Initialize-Drive-Parameters	91H	-	Y	-	-	Y	-
Read-DMA	C8H or C9H	-	Y	Y	Y	Y	Y
Read-Multiple	C4H	-	Y	Y	Y	Y	Y
Read-Sector(s)	20H or 21H	-	Y	Y	Y	Y	Y
Read-Verify-Sector(s)	40H or 41H	-	Y	Y	Y	Y	Y
Recalibrate	10H	-	-	-	-	D	-
Security-Disable-Password	F6H	-	-	-	-	D	-
Security-Erase-Prepare	F3H	-	-	-	-	D	-
Security-Erase-Unit	F4H	-	-	-	-	D	-
Security-Freeze-Lock	F5H	-	-	-	-	D	-
Security-Set-Password	F1H	_	-	-	-	D	-
Security-Unlock	F2H	_	-	-	-	D	-
Seek	7XH	-	-		Y	Y	
Set-Features	EFH	Y <sup>7</sup>	-	-	-	D	-



#### Table 2-1: Command set (2 of 2)

Command	Code	<b>FR</b> <sup>1</sup>	SC <sup>2</sup>	SN <sup>3</sup>	CY <sup>4</sup>	DH⁵	LBA <sup>6</sup>
Set-Multiple-Mode	C6H	-	Y	-	-	D	-
Sleep	E6H	-	-	-	-	D	-
SMART	B0H	Y	Y	Y	Y	D	
Standby	E2H	-	_	-	-	D	-
Standby-Immediate	E0H	-	-	-	-	D	-
Write-DMA	CAH	-	Y	Y	Y	Y	Y
Write-Multiple	C5H	-	Y	Y	Y	Y	Y
Write-Sector(s)	30H	-	Y	Y	Y	Y	Y

1. FR - Features register

2. SC - Sector Count register

3. SN - Sector Number register

4. CY - Cylinder registers5. DH - Drive/Head register

6. LBA - Logical Block Address mode supported (see command descriptions for use)7. Y - The register contains a valid parameter for this command.

8. For the Drive/Head register: Y means both the SAFD and Head parameters are used

D means only the SAFD parameter is valid and not the Head parameter



#### 2.2 S.M.A.R.T. Technology

S.M.A.R.T. is an acronym for Self-Monitoring, Analysis and Reporting Technology, an open standard allowing disk drives to automatically monitor their own health and report potential problems. It protects the user from unscheduled downtime by monitoring and storing critical drive performance and calibration parameters. Ideally, this should allow taking proactive actions to prevent impending drive failure.

Apacer SAFD25M2 uses the standard SMART command B0h to read data from the drive for SMART feature as the SATA Rev.2.6 ATA/ATAPI-7 specifications. Based on the SFF-8035i Rev. 2.0 specifications, Apacer SMART defines 3 vendor-specified SMART Attribute IDs (E5h, EAh-EBh, and E8h) in the SAFD25M2. They represent Flash ID, maximum erase count, average erase count, good block count, free-list block count, and firmware version information. When the Apacer SMART Utility running on the host, it analyzes and reports the disk status to the host before the SAFD25M2 is in critical condition.



## **3. Flash Management**

#### 3.1 Error Correction/Detection

The SAFD25M2 implements a hardware ECC scheme, based on the BCH algorithm. It can detect and correct up to 8 bits or 15 bits error in 512 bytes.

#### 3.2 Bad Block Management

Although bad blocks on the flash media are already identified by the flash manufacturer, they can also be accumulated over time during operation. The SAFD25M2's controller maintains a table that lists those normal blocks with disk data, the free blocks for wear leveling, and bad blocks with errors. When a normal block is detected broken, it is replaced with a free block and listed as a bad block. When a free block is detected broken, it is then removed from the free block list and marked as a bad block.

During device operation, this ensures that newly accumulated bad blocks are transparent to the host. The device will stop file write service once there are only two free blocks left such that the read function is still available for copying the files from the disk into another.

#### 3.3 Wear Leveling

The NAND flash devices are limited by a certain number of write cycles. When using a FAT-based file system, frequent FAT table updates are required. If some area on the flash wears out faster than others, it would significantly reduce the lifetime of the whole SSD, even if the erase counts of others are far from the write cycle limit. Thus, if the write cycles can be distributed evenly across the media, the lifetime of the media can be prolonged significantly. This scheme is called wear leveling.

Apacer's wear-leveling scheme is achieved both via buffer management and Apacer-specific static wear leveling. They both ensure that the lifetime of the flash media can be increased, and the disk access performance is optimized as well.

#### 3.4 **Power Failure Management**

The Low Power Detection on the controller initiates crucial data saving before the power supplied to the device is too low. This feature prevents the device from crash and ensures data integrity during an unexpected power-off.

#### 3.5 Quick Erase

Accomplished by the Secure Erase (SE) command, which added to the open ANSI standards that control disk drives, "Quick Erase" is built into the disk drive itself and thus far less susceptible to malicious software attacks than external software utilities. It is a positive easy-to-use data destroy command, amounting to electronic data shredding. Executing the command causes a drive to internally completely erase all possible user data. This command is carried out within disk drives, so no additional software is required. Once executed, neither data nor the erase counter on the device would be recoverable, which blurs the accuracy of device lifespan. The process to erase will not be stopped until finished while encountering power failure, and will be continued when power is back on.



# 4. Environmental Specifications

#### 4.1 Environments

The SAFD25M2 environmental specifications follow the US Military Standard MIL-STD-810F, as shown in Table 4-1.

Environment	Specification
Tanananatan	0°C to 70°C (Operating – Standard)
Temperature	-40°C to 100°C (Non-operating)
Humidity	5% to 95% RH (Non-condensing)
Vibration	Sine wave: 5~55~5 Hz (X, Y, Z) Random: 10-2000 Hz, 16.3 G (X, Y, Z)
Shock	Acceleration: 1,500 G, 0.5 ms Peak acceleration: 50 G, 11 ms

**Table 4-1** SAFD25M2 environmental specifications (plan)



# **5. Electrical Specification**

*Caution:* Absolute Maximum Stress Ratings – (Applied conditions greater than those listed under "Absolute Maximum Stress Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these conditions or conditions greater than those defined in the operational sections of this data sheet is not implied. Exposure to absolute maximum stress rating conditions may affect device reliability.

 Table 5-1:
 Operating voltage

Range	Ambient Temperature	5V
Standard	0°C to 70°C	4.5-5.5V

 Table 5-2:
 Absolute maximum power pin stress ratings

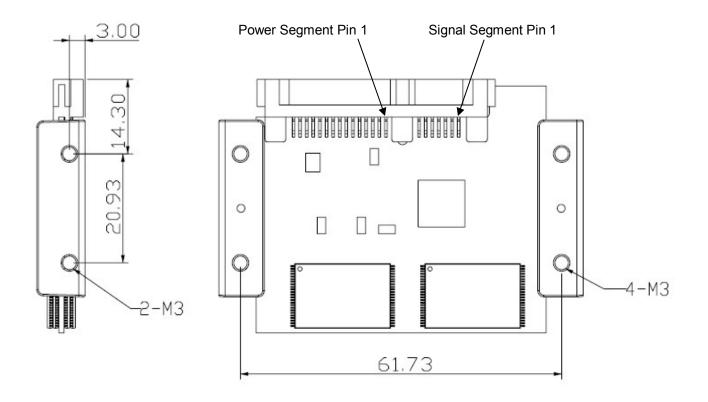
Parameter	Symbol	Conditions
Input Power	V <sub>DD</sub>	-0.3V min. to 6.5V max.
Voltage on any pin except $V_{DD}$ with respect to GND	V	-0.5V min. to VDD + 0.5V max.

#### Serial ATA Flash Drive AP-SAFD25MPAxxxS-E



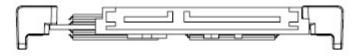
# **6. Physical Characteristics**

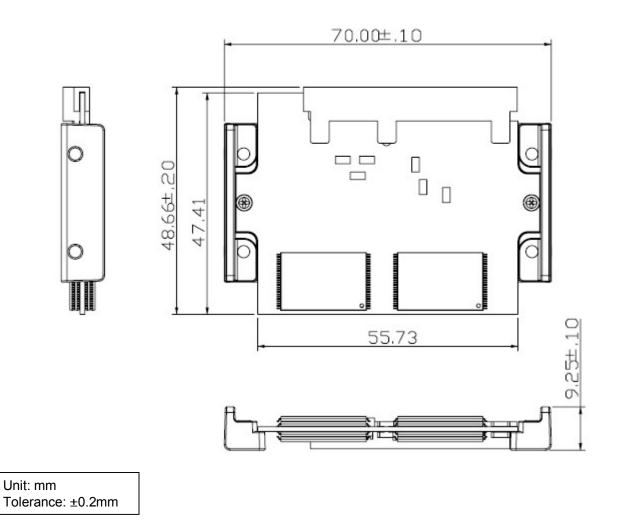
## 6.1 Dimension



Unit: mm Tolerance: ±0.2mm



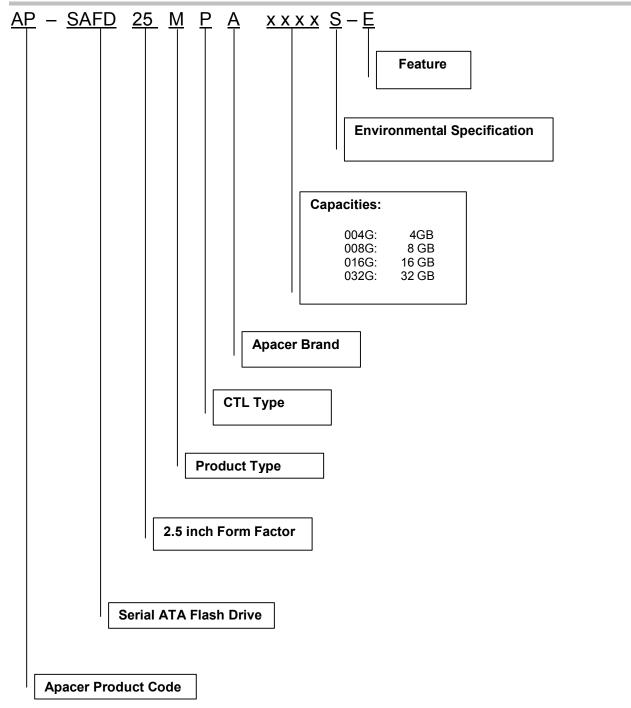






# 7. Product Ordering Information

## 7.1 Product Code Designations





## 7.2 Valid Combinations

Capacity	Model Number
4GB	AP-SAFD25MPA004GS-E
8GB	AP-SAFD25MPA008GS-E
16GB	AP-SAFD25MPA016GS-E
32GB	AP-SAFD25MPA032GS-E

**Note:** Valid combinations are those products in mass production or will be in mass production. Consult your Apacer sales representative to confirm availability of valid combinations and to determine availability of new combinations.

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# **Revision History**

Revision	Date	Description	Remark
1.0	03/30/2009	Official release	
1.1	04/27/2009	Updated CHS values	
1.2	06/18/2009	Supplemented quick erase related information	
1.3	08/21/2009	Updated feature item	
1.4	12/10/2009	Updated models & Changed coding rule	



## **Global Presence**

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