## AIDW40S65C5

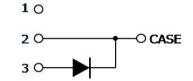


650V/40A Silicon Carbide Schottky Diode in TO247-3

#### Features

- Revolutionary semiconductor material Silicon Carbide
- Benchmark switching behavior
- No reverse recovery/ No forward recovery
- Temperature independent switching behavior
- High surge current capability
- Pb-free lead plating; RoHS compliant
- Junction Temperature range from -40°C to 175°C
- System efficiency improvement over Si diodes
- System cost / size savings due to reduced cooling requirements
- Enabling higher frequency / increased power density solutions
- Higher system reliability due to lower operating temperatures
- Reduced EMI





RoHS

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#### **Potential Applications**

- Traction inverter
- Booster / DCDC Converter
- On board Charger / PFC

#### **Product Validation**

"Qualified for Automotive Applications. Product Validation according to AEC-Q100/101"

#### Description

The 5th Generation CoolSiC<sup>™</sup> Automotive Schottky Diode represents Infineon leading edge technology for Silicon Carbide Schottky Barrier diodes. Thanks to a compact design and a technology based on thin wafers, this family of products shows improved efficiency over all load conditions resulting from both its thermal characteristics and low figure of merit (Qc x Vf). This product family has been designed to complement Infineon's IGBT and CoolMOS<sup>™</sup> portfolio. This ensures meeting the most stringent application requirements in the 650V voltage class.

🔁 Green

Product Information				
Ordering Code	AIDW40S65C5			
Marking	AD4065C5			
Package	PG-TO247-3-41			
SP Number	SP001725204			

Parameter	Value/Unit				
V <sub>DC,max</sub>	650 V				
I <sub>F</sub> ; T <sub>C</sub> < 117 °C	40 A				
$Q_{\rm C}; V_{\rm R}$ = 400 V	56 nC				
E <sub>C</sub> ; V <sub>R</sub> = 400 V	12.9 µJ				
T <sub>j,max</sub>	175 °C				

Pin	Definition
Pin 2, case	Cathode
Pin 3	Anode



Table of Contents

### Table of Contents

Featu	ıres	1
Poter	ntial Applications	1
Produ	uct Validation	1
Descr	ription	1
Table	of Contents	2
1	Maximum Ratings	3
2	Thermal Characteristics	4
3	Electrical Characteristics	5
4	Electrical Characteristics Diagrams	6
	Package Outlines	
	ion History	



Maximum Ratings

## 1 Maximum Ratings

Table 1Maximum ratings1

Parameter	Symbol	Value	Unit
Repetitive peak reverse voltage	V <sub>RRM</sub>	650	V
Continuous forward current for $R_{thJC,max}$ T <sub>C</sub> = 117 °C, D=1	I <sub>F</sub>	40	А
Surge non-repetitive forward current, sine halfwave T <sub>c</sub> = 25°C, t <sub>p</sub> =10ms T <sub>c</sub> = 150°C, t <sub>p</sub> =10ms	I <sub>F,SM</sub>	182 153	A
Non-repetitive peak forward current T <sub>C</sub> = 25°C, t <sub>p</sub> =10µs	I <sub>F,max</sub>	1432	А
$i^{2}t$ value T <sub>c</sub> = 25°C, t <sub>p</sub> =10ms T <sub>c</sub> = 150°C, t <sub>p</sub> =10ms	∫i <sup>2</sup> dt	166 118	A <sup>2</sup> s
Diode dv/dt ruggedness V <sub>R</sub> =0480V	dv/dt	100	V/ns
Power dissipation T <sub>c</sub> = 25°C	P <sub>tot</sub>	183	W
Operating temperature	Tj	-40175	°C
Storage temperature	T <sub>stg</sub>	-55150	°C
ESD Human body model, R= 1.5 k $\Omega$ , C = 100 pF Charged device model		8 2	kV
Soldering temperature, wavesoldering only allowed at leads, 1.6mm (0.063 in.) from case for 10 s	T <sub>sold</sub>	260	°C
Mounting Torque (M3 and M4 screws)		70	Ncm



**Thermal Characteristics** 

## 2 Thermal Characteristics

Table 2Thermal Characteristics1

Darameter	Symbol	Values			110:4	Note /Test condition
Parameter		Min.	Тур.	Max.	Unit	Note/Test condition
Thermal resistance, junction–case <sup>2</sup>	$R_{thJC}$	-	0.6	0.8	K/W	
Thermal resistance, junction-ambient <sup>2</sup>	$R_{thJA}$	-	I	62	K/W	



**Electrical Characteristics** 

### 3 Electrical Characteristics

#### Table 3Static Characteristics

Devementer	Symbol	Values			11	Noto /Tost oon dition
Parameter		Min.	Тур.	Max.	Unit	Note/Test condition
DC blocking voltage	V <sub>DC</sub>	650	-	-		T <sub>j</sub> = 25°C, I <sub>R</sub> = 0.12 mA
Diode forward voltage <sup>3</sup>	V <sub>F</sub>	-	1.5	1.7	v	T <sub>j</sub> = 25°C, I <sub>F</sub> = 40 A
		-	1.8	2.1		T <sub>j</sub> = 150°C, I <sub>F</sub> = 40 A
Reverse current	I <sub>R</sub>	-	7	120		V <sub>R</sub> = 650 V, T <sub>j</sub> = 25 °C
		-	47	-	μA	V <sub>R</sub> = 650 V, T <sub>j</sub> = 150 °C

#### Table 4Dynamic Characteristics at Tj=25°C unless noted otherwise

Deremeter	Symbol	Values			Unit	Noto /Test condition
Parameter		Min.	Тур.	Max.	Unit	Note/Test condition
Total capacitive charge	Qc	-	56	-	nC	$V_{R} = 400 \text{ V, } \text{di/dt} = 200 \text{ A/}\mu\text{s,}$ $I_{F} \le I_{F,MAX}, T_{j} = 150 \text{ °C}$
	С	-	1138	-	рF	V <sub>R</sub> = 1 V, f = 1 MHz
Total capacitance		-	148	-		V <sub>R</sub> = 300 V, f = 1 MHz
		-	145	-		V <sub>R</sub> = 600 V, f = 1 MHz

#### Footnotes:

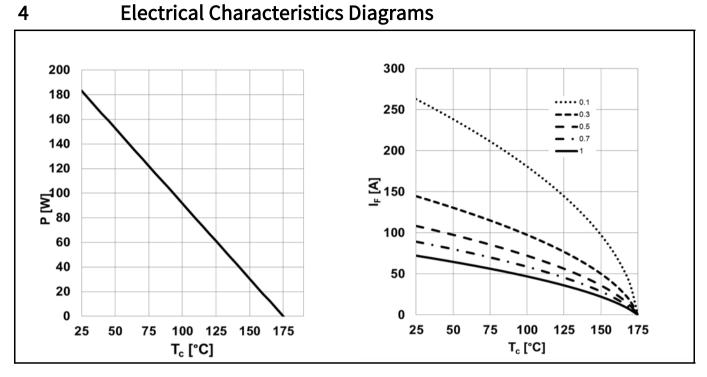
<sup>1</sup> The parameter is not subject to production test- verified by design/characterization.

<sup>2</sup> Rth, JC defined as per JESD-51-14. Rth, JA defined as per JESD-51-2.

<sup>3</sup> Only the value at 25°C is subject to production test. The value at 150°C is only verified by design/characterization.

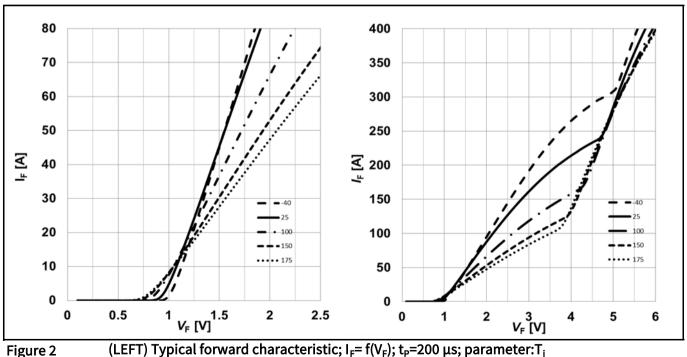


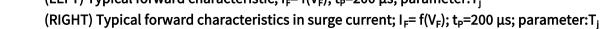
**Electrical Characteristics Diagrams** 





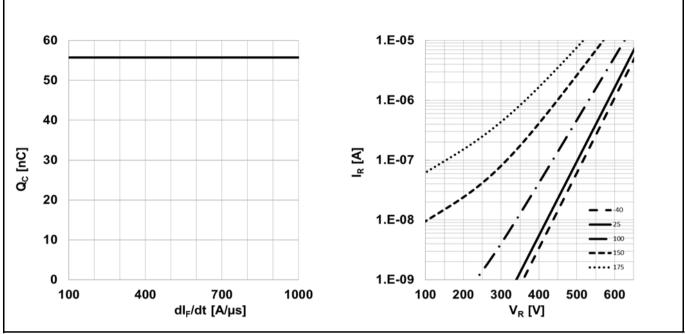
(LEFT) Power dissipation;  $P_{tot} = f(T_C)$ ;  $R_{thJC,max}$ (RIGHT) Diode forward current;  $I_F = f(T_C)$ ;  $T_j \le 175$  °C;  $R_{thJC,max}$ ; parameter: D=duty cycle

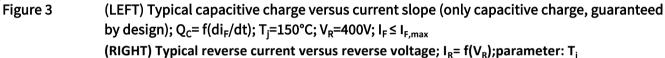


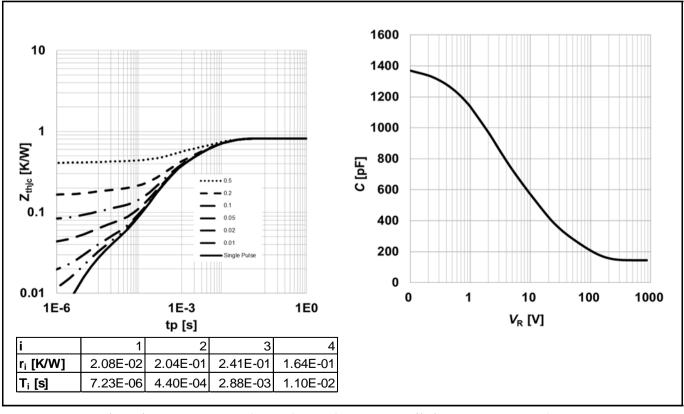




#### **Electrical Characteristics Diagrams**









(LEFT) Max. Transient thermal impedance;  $Z_{thJC} = f(t_P)$ ; parameter:D= $t_P/T$  (RIGHT) Typ. Capacitance vs. Reverse voltage; C=  $f(V_R)$ ;  $T_i = 25^{\circ}$ C; f=1 MHz



#### **Electrical Characteristics Diagrams**

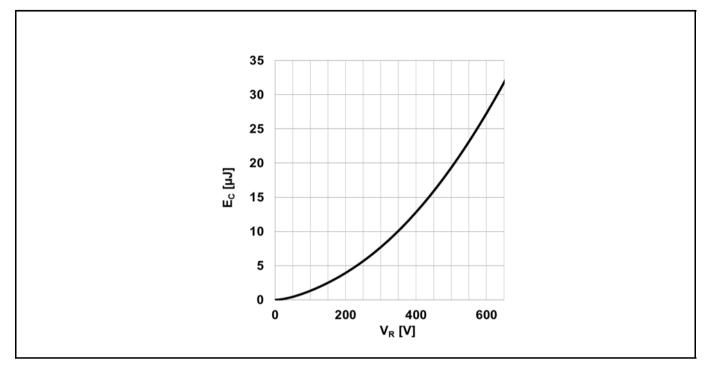
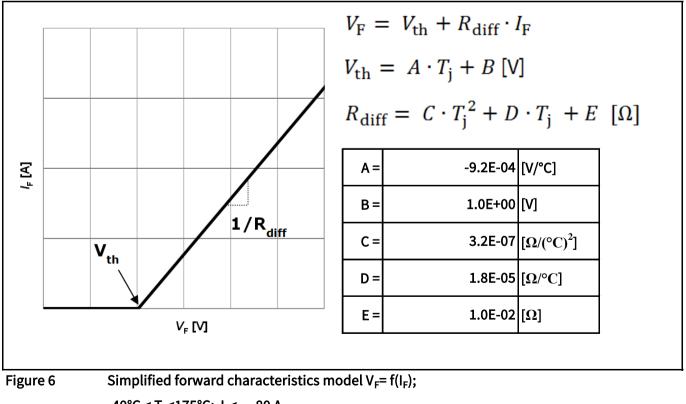


Figure 5 Typical capacitance stored energy;  $E_c = f(V_R)$ 



-40°C < T<sub>i</sub> <175°C; I<sub>F</sub>< 80 A



#### Package Outlines

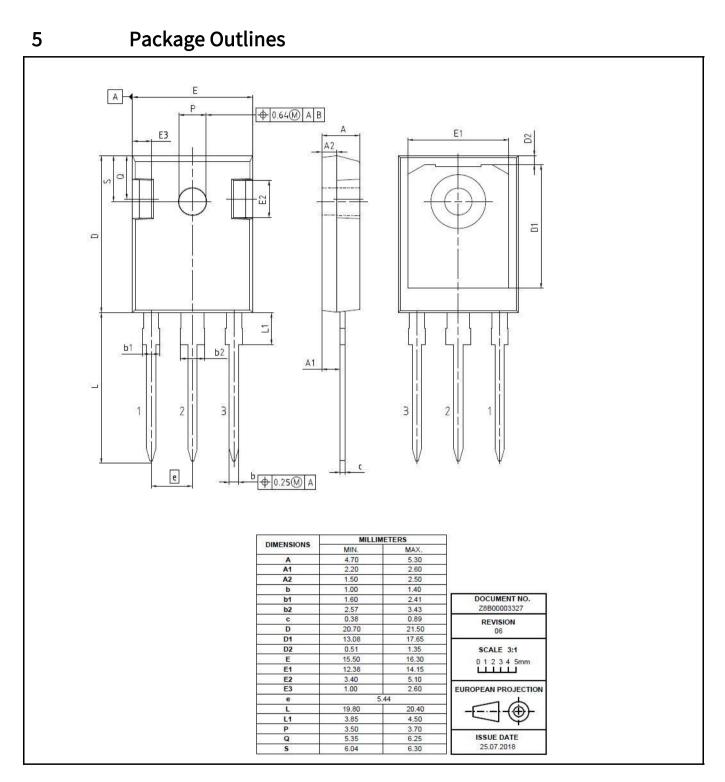


Figure 6

Package outline of PG-TO247-3-41 leaded (Dimensions in mm)



**Revision History** 

### **Revision History**

Document Version	Date of Release	Description of changes			
V3.0	26.11.2018	1st release of Data Sheet			



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