

TO-92

EBC

C SOT-23 Mark:2B

# **Ordering Information**

FAIRCHILD

Part Number	Top Mark	Package	Packing Method
PN2907BU	PN2907	TO-92 3L	Bulk
MMBT2907_D87Z	2B	SOT-23 3L	Tape and Reel

January 2014

## Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	-40	V
V <sub>CBO</sub>	Collector-Base Voltage	-60	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5.0	V
Ι <sub>C</sub>	Collector Current - Continuous	-800	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

# **Thermal Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Ma	Unit	
		PN2907 <sup>(3)</sup>	MMBT2907 <sup>(4)</sup>	onit
р	Total Device Dissipation	625	350	mW
PD	Derate Above 25°C	5.0	2.8	mW/°C
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case	83.3		°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	200	357	°C/W

## Notes:

3. PCB size: FR-4 76 x 114 x 1.57 mm<sup>3</sup> (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

4. Device mounted on FR-4 PCB 1.6. inch x 1.6 inch x 0.06 inch.

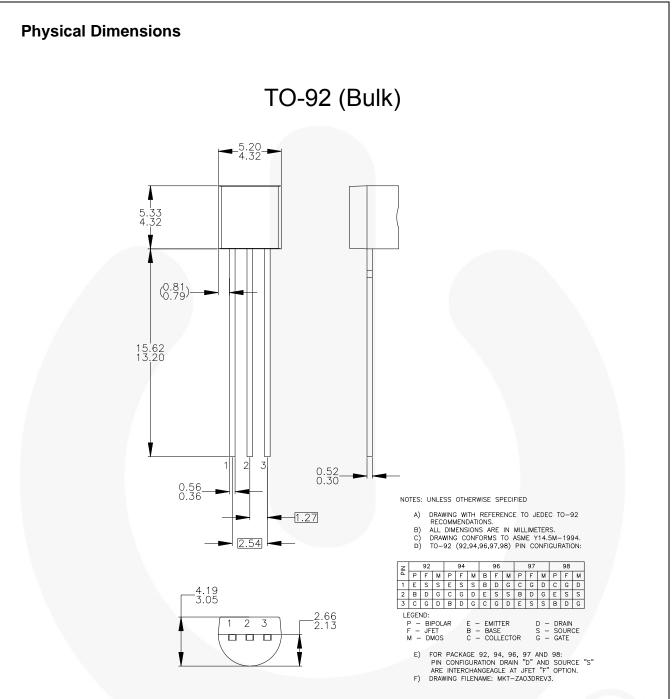
**Electrical Characteristics** 

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
Off Charac	cteristics	· ·			-
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage <sup>(5)</sup>	I <sub>C</sub> = -10 mA, I <sub>B</sub> = 0	-40		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -10 μA, I <sub>E</sub> = 0	-60		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -10 μA, I <sub>C</sub> = 0	-5.0		V
I <sub>CEX</sub>	Collector Cut-Off Current	$V_{CE}$ = -30 V, $V_{EB}$ = -0.5 V		-50	nA
I <sub>BL</sub>	Base Cut-Off Current	$V_{CE}$ = -30 V, $V_{EB}$ = -0.5 V		-50	nA
1	Collector Cut-Off Current	$V_{CB} = -50 \text{ V}, \text{ I}_{E} = 0$		-20	nA
I <sub>CBO</sub>		V <sub>CB</sub> = -50 V, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C		-20	μΑ
On Charac	cteristics <sup>(5)</sup>				
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = -0.1 mA	35		
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = -1.0 mA	50		
h <sub>FE</sub>	DC Current Gain	V <sub>CE</sub> = -10 V, I <sub>C</sub> = -10 mA	70		
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = -150 mA	100	300	
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = -500 mA	30		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA		-0.4	- V
		I <sub>C</sub> = -500 mA, I <sub>B</sub> = -50 mA		-1.6	
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = -150 mA, I <sub>B</sub> = -15 mA		-1.3	v
vBE(3at)		$I_{\rm C}$ = -500 mA, $I_{\rm B}$ = -50 mA		-2.6	v
Small Sigr	nal Characteristics				
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -10 V, f = 1.0 MHz		8	pF
C <sub>ib</sub>	Input Capacitance	V <sub>EB</sub> = -2.0 V, f = 1.0 MHz		30	pF
h <sub>fe</sub>	Small-Signal Current Gain	I <sub>C</sub> = -50 mA, V <sub>CE</sub> = -20 V, f = 100 MHz	2		
Switching	Characteristics				
t <sub>on</sub>	Turn-On Time			45	ns
t <sub>d</sub>	Delay Time	V <sub>CC</sub> = -30 V, I <sub>C</sub> = -150 mA, I <sub>B1</sub> = -15 mA		10	ns
t <sub>r</sub>	Rise Time			40	ns
t <sub>off</sub>	Turn-Off Time	N 00X1 150 1		100	ns
t <sub>s</sub>	Storage Time	$V_{CC} = -6.0 \text{ V}, I_C = -150 \text{ mA},$ $I_{B1} = I_{B2} = -15 \text{mA}$		80	ns
t <sub>f</sub>	Fall Time			30	ns

Note:

5. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2.0%.



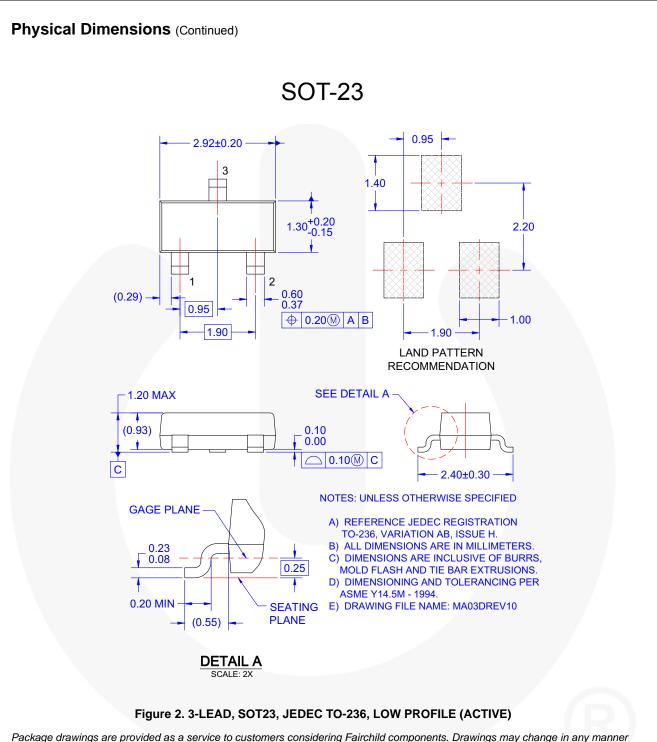
## Figure 1. 3-LEAD, TO92, JEDEC TO-92 COMPLIANT STRAIGHT LEAD CONFIGURATION (OLD TO92AM3)

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PN2907 / MMBT2907 — PNP General-Purpose Transistor

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