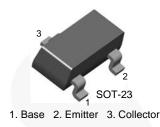


BC846 / BC847 / BC848 / BC850 NPN Epitaxial Silicon Transistor

Features

- Switching and Amplifier Applications
- Suitable for Automatic Insertion in Thick and Thin-film Circuits
- Low Noise: BC850
- Complement to BC856, BC857, BC858, BC859, and BC860



Ordering Information⁽¹⁾

Part Number	Marking	Package	Packing Method
BC846AMTF	8AA	SOT-23 3L	Tape and Reel
BC846BMTF	8AB	SOT-23 3L	Tape and Reel
BC846CMTF	8AC	SOT-23 3L	Tape and Reel
BC847AMTF	8BA	SOT-23 3L	Tape and Reel
BC847BMTF	8BB	SOT-23 3L	Tape and Reel
BC847CMTF	8BC	SOT-23 3L	Tape and Reel
BC848BMTF	8CB	SOT-23 3L	Tape and Reel
BC848CMTF	8CC	SOT-23 3L	Tape and Reel
BC850AMTF	8EA	SOT-23 3L	Tape and Reel
BC850CMTF	8EC	SOT-23 3L	Tape and Reel

Note:

1. Affix "-A,-B,-C" means h_{FE} classification. Affix "-M" means SOT-23 package. Affix "-TF" means the tape and reel type packing.

August 2015

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parame	Value	Unit	
		BC846	80	
V _{CBO}	Collector-Base Voltage	BC847 / BC850	50	V
		BC848	30	
		BC846	65	
V _{CEO}	Collector-Emitter Voltage	BC847 / BC850	45	V
		BC848	30	
V	Emitter-Base Voltage	BC846 / BC847	6	V
V _{EBO} En		BC848 / BC850	5	v
Ι _C	Collector Current (DC)		100	mA
TJ	Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-65 to +150	°C

Thermal Characteristics⁽²⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
р	Power Dissipation	310	mW
PD	Derate Above 25°C	2.48	mW/°C
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient	403	°C/W

Note:

2. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

BC846 / BC847 / BC848 / BC850 — NPN Epitaxial Silicon Transistor

Electrical Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

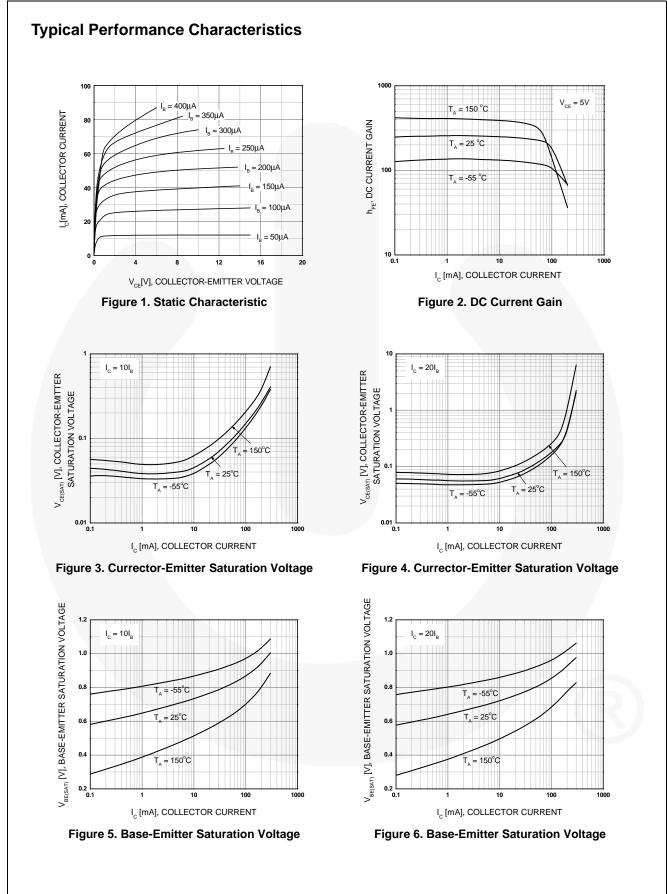
Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector Cut-Off Current		V _{CB} = 30 V, I _E = 0			15	nA
h _{FE}	DC Current Gain		$V_{CE} = 5 \text{ V}, I_{C} = 2 \text{ mA}$	110		800	
V _{CE} (sat)	Collector-Emitter Saturation		I _C = 10 mA, I _B = 0.5 mA		90	250	
VCE(Sal)	Voltage		I _C = 100 mA, I _B = 5 mA		200	600	mV
V _{RE} (sat) Collector-Base Saturation Voltage			I _C = 10 mA, I _B = 0.5 mA		700		mV
V _{BE} (sat)	Collecto	I-Dase Saluration voltage	I _C = 100 mA, I _B = 5 mA		900		mv
V (op)	Booo Er	oittor On Valtaga	$V_{CE} = 5 \text{ V}, I_{C} = 2 \text{ mA}$	580	660	700	m\/
v _{BE} (OII)	V _{BE} (on) Base-Emitter On Voltage		$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$			720	mV
f _T	Current Gain Bandwidth Product		V _{CE} = 5 V, I _C = 10 mA, f = 100 MHz		300		MHz
C _{ob}	Output Capacitance		V _{CB} = 10 V, I _E = 0, f = 1 MHz		3.5	6.0	pF
C _{ib}	Input Capacitance		$V_{EB} = 0.5 \text{ V}, I_{C} = 0, f = 1 \text{ MHz}$		9		pF
		BC846 / BC847 / BC848	$V_{CE} = 5 \text{ V}, I_{C} = 200 \mu\text{A},$		2.0	10.0	dB
NE	Noise	BC850	$R_G = 2 k\Omega, f = 1 kHz$		1.2	4.0	
	Figure	BC850	$V_{CE} = 5$ V, I _C = 200 μA, R _G = 2 kΩ, f = 30 to 15000 Hz		1.4	3.0	

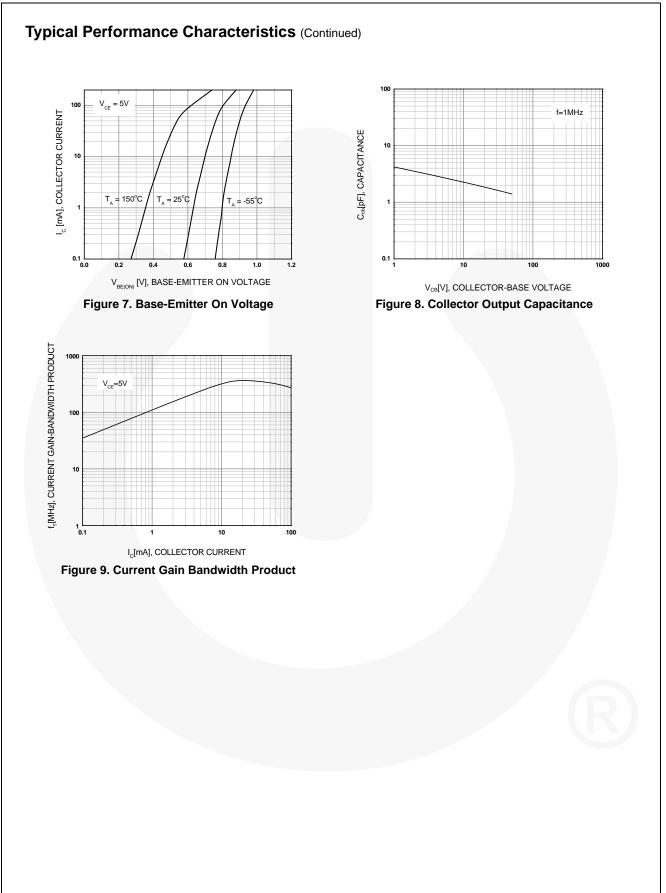
Note:

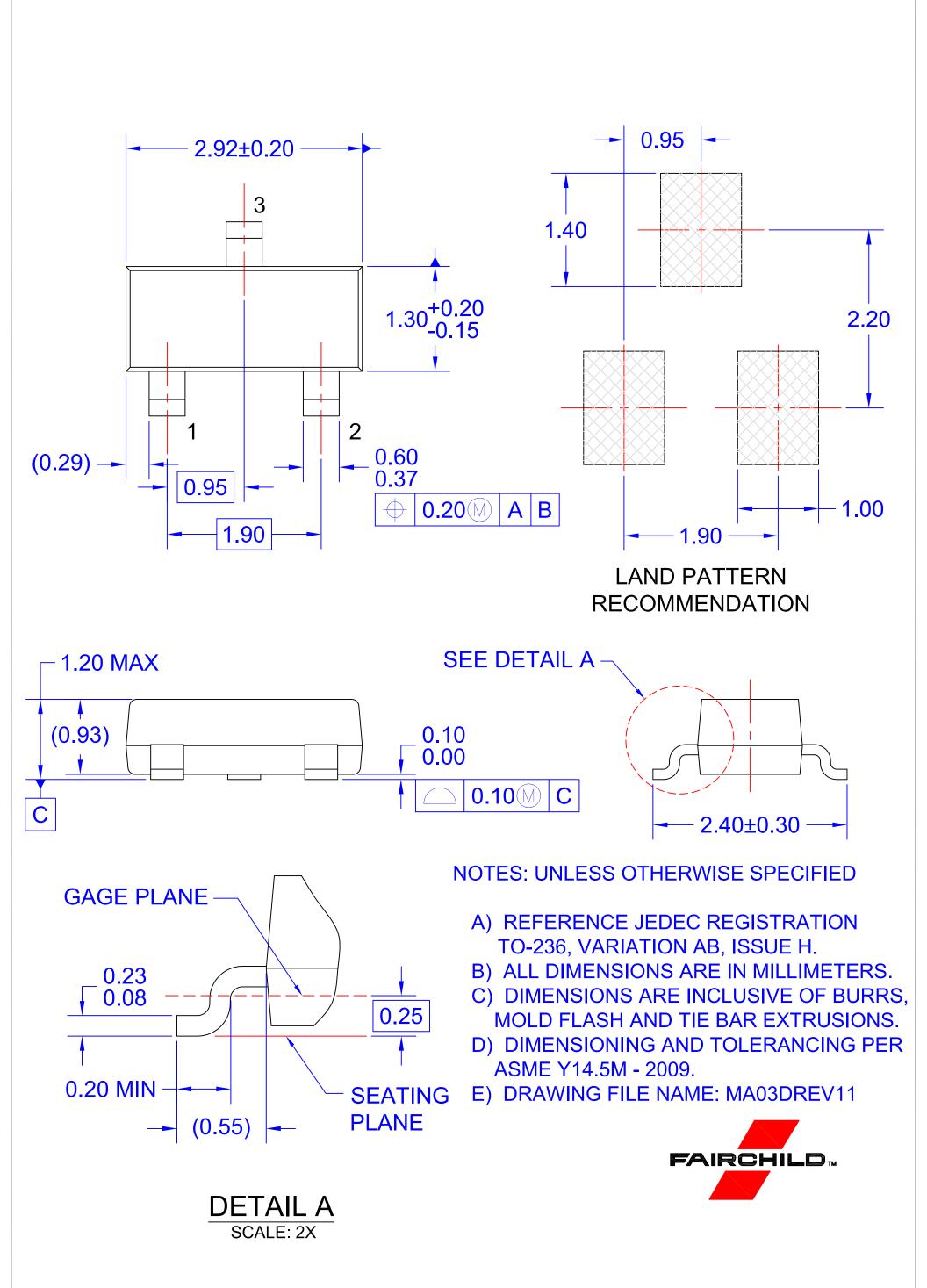
3. Pulse test: pulse width \leq 300 $\mu s,$ duty cycle \leq 2%

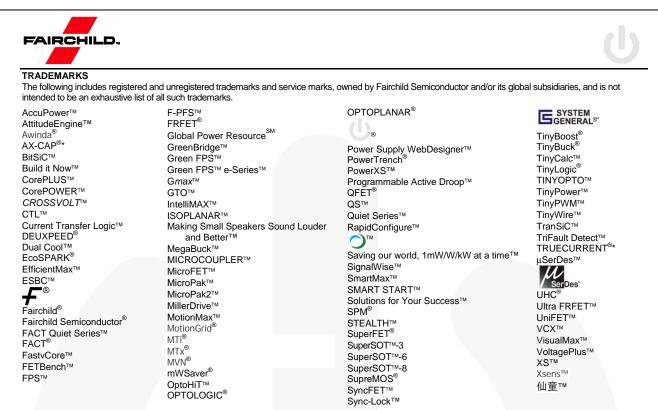
h_{FE} Classification

Classification	Α	В	С	
h _{FE}	110 ~ 220	200 ~ 450	420 ~ 800	









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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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