Discrete POWER & Signal **Technologies** 

# 2N5830



2N5830



### NPN General Purpose Amplifier

This device is designed for general purpose high voltage amplifiers and gas discharge display driving. Sourced from Process 16. See 2N5551 for characteristics.

#### **Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CEO</sub>	Collector-Emitter Voltage	100	V	
V <sub>CBO</sub>	Collector-Base Voltage	120	V	
$V_{\text{EBO}}$	Emitter-Base Voltage	5.0	V	
Ic	Collector Current - Continuous	200	mA	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C	

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.
 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

#### **Thermal Characteristics** TA = 25°C unless otherwise noted

	TA = 25 C unless otherwise	siloted
Characteris	tic	Мах

Symbol	Characteristic	Max	Units
		2N5830	
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	mW mW/°C
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	83.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W

# NPN General Purpose Amplifier (continued)

Symbol	Parameter	Test Conditions	Min	Мах	Units
OFF CHA	RACTERISTICS				
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage*	$I_{\rm C} = 1.0 \text{ mA}, I_{\rm B} = 0$	100		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = 100 \ \mu {\rm A}, \ I_{\rm E} = 0$	120		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_{\rm E} = 10 \mu {\rm A},  I_{\rm C} = 0$	5.0		V
СВО	Collector Cutoff Current	$V_{CB} = 100 \text{ V}, I_E = 0$		50	nA
CBU				25	μA
I <sub>EBO</sub> ON CHAF	Emitter Cutoff Current RACTERISTICS*	$V_{CB} = 100 \text{ V}, I_E = 0, T_A = 100 \text{ °C}$ $V_{EB} = 4.0 \text{ V}, I_C = 0$		25 50	μA nA
EBO ON CHAF	Emitter Cutoff Current RACTERISTICS*	$V_{CB} = 100 \text{ V}, I_E = 0, T_A = 100 \text{ °C}$ $V_{EB} = 4.0 \text{ V}, I_C = 0$	60		
EBO ON CHAF	Emitter Cutoff Current	$V_{CB} = 100 \text{ V}, I_E = 0, T_A = 100 ^{\circ}\text{C}$	60 80		
EBO ON CHAF	Emitter Cutoff Current ACTERISTICS* DC Current Gain	$\begin{split} & V_{CB} = 100 \text{ V}, \text{ I}_{E} = 0, \text{ T}_{A} = 100 ^{\circ}\text{C} \\ & V_{EB} = 4.0 \text{ V}, \text{ I}_{C} = 0 \end{split}$		50 500	nA
ево ON CHAF	Emitter Cutoff Current RACTERISTICS*	$\begin{split} & V_{CB} = 100 \text{ V}, \text{ I}_{E} = 0, \text{ T}_{A} = 100 ^{\circ}\text{C} \\ & V_{EB} = 4.0 \text{ V}, \text{ I}_{C} = 0 \end{split}$	80	50 500 0.15	nA NA
EBO ON CHAF	Emitter Cutoff Current ACTERISTICS* DC Current Gain	$\begin{split} & V_{CB} = 100 \text{ V}, \text{ I}_{E} = 0, \text{ T}_{A} = 100 ^{\circ}\text{C} \\ & V_{EB} = 4.0 \text{ V}, \text{ I}_{C} = 0 \\ \end{split} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 1.0 \text{ mA} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 10 \text{ mA} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 50 \text{ mA} \\ & \text{ I}_{C} = 1.0 \text{ mA}, \text{ I}_{B} = 0.1 \text{ mA} \\ & \text{ I}_{C} = 10 \text{ mA}, \text{ I}_{B} = 1.0 \text{ mA} \end{split}$	80	50 500 0.15 0.2	nA V V
EBO ON CHAF MFE VCE(sat)	Emitter Cutoff Current CACTERISTICS* DC Current Gain Collector-Emitter Saturation Voltage	$\begin{split} & V_{CB} = 100 \text{ V}, \text{ I}_{E} = 0, \text{ T}_{A} = 100 ^{\circ}\text{C} \\ & V_{EB} = 4.0 \text{ V}, \text{ I}_{C} = 0 \\ \end{split} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 1.0 \text{ mA} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 10 \text{ mA} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 50 \text{ mA} \\ & \text{ I}_{C} = 1.0 \text{ mA}, \text{ I}_{B} = 0.1 \text{ mA} \\ & \text{ I}_{C} = 10 \text{ mA}, \text{ I}_{B} = 1.0 \text{ mA} \\ & \text{ I}_{C} = 50 \text{ mA}, \text{ I}_{B} = 5.0 \text{ mA} \end{split}$	80	50 500 0.15 0.2 0.25	nA V V V
ON CHAF	Emitter Cutoff Current ACTERISTICS* DC Current Gain	$\begin{split} & V_{CB} = 100 \text{ V}, \text{ I}_{E} = 0, \text{ T}_{A} = 100 ^{\circ}\text{C} \\ & V_{EB} = 4.0 \text{ V}, \text{ I}_{C} = 0 \\ \end{split} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 1.0 \text{ mA} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 10 \text{ mA} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 50 \text{ mA} \\ & \text{ I}_{C} = 1.0 \text{ mA}, \text{ I}_{B} = 0.1 \text{ mA} \\ & \text{ I}_{C} = 10 \text{ mA}, \text{ I}_{B} = 1.0 \text{ mA} \end{split}$	80	50 500 0.15 0.2	nA V V
EBO	Emitter Cutoff Current CACTERISTICS* DC Current Gain Collector-Emitter Saturation Voltage	$\begin{split} & V_{CB} = 100 \text{ V}, \text{ I}_{E} = 0, \text{ T}_{A} = 100 ^{\circ}\text{C} \\ & V_{EB} = 4.0 \text{ V}, \text{ I}_{C} = 0 \\ \end{split} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 1.0 \text{ mA} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 10 \text{ mA} \\ & V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 50 \text{ mA} \\ & \text{ I}_{C} = 1.0 \text{ mA}, \text{ I}_{B} = 0.1 \text{ mA} \\ & \text{ I}_{C} = 10 \text{ mA}, \text{ I}_{B} = 5.0 \text{ mA} \\ & \text{ I}_{C} = 1.0 \text{ mA}, \text{ I}_{B} = 0.1 \text{ mA} \\ & \text{ I}_{C} = 1.0 \text{ mA}, \text{ I}_{B} = 0.1 \text{ mA} \\ & \text{ I}_{C} = 1.0 \text{ mA}, \text{ I}_{B} = 0.1 \text{ mA} \\ & \text{ I}_{C} = 1.0 \text{ mA}, \text{ I}_{B} = 0.1 \text{ mA} \end{split}$	80	50 500 0.15 0.2 0.25 0.8	nA V V V V

C <sub>cb</sub>	Output Capacitance	$V_{CB} = 10 \text{ V}, \text{ f} = 1.0 \text{ MHz}$		4.0	pF
h <sub>fe</sub>	Small-Signal Current Gain	$I_{C} = 10 \text{ mA}, V_{CE} = 10 \text{ V},$ f = 100 MHz	1.0	5.0	
h <sub>ie</sub>	Input Impedance	$I_{C} = 1.0 \text{ mA}, V_{CE} = 10 \text{ V},$		6.0	KΩ
h <sub>oe</sub>	Output Admittance	f = 1.0 kHz		40	μmho
h <sub>fe</sub>	Small-Signal Current Gain		60		

\*Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

2N5830



©2001 Fairchild Semiconductor Corporation

March 2001, Rev. B1





July 1999, Rev. A



#### TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™ Bottomless™ CoolFET™ CROSSVOLT™ DOME™ E<sup>2</sup>CMOS<sup>™</sup> EnSigna™ FACT™ FACT Quiet Series<sup>™</sup> FAST<sup>®</sup>

FASTr™ GlobalOptoisolator™ GTO™ HiSeC™ **ISOPLANAR™** MICROWIRE™ OPTOLOGIC™ **OPTOPLANAR™** PACMAN™ POP™

PowerTrench<sup>®</sup> QFET™ QS™ QT Optoelectronics<sup>™</sup> Quiet Series<sup>™</sup> SILENT SWITCHER® SMART START™ SuperSOT<sup>™</sup>-3 SuperSOT<sup>™</sup>-6 SuperSOT<sup>™</sup>-8

SyncFET™ TinyLogic™ UHC™ VCX™

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### PRODUCT STATUS DEFINITIONS

**Definition of Terms** 

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.
	1	Rev G

## **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Fairchild Semiconductor: