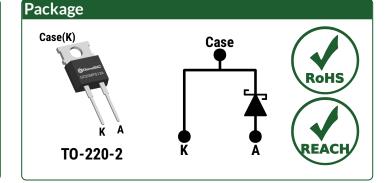
Silicon Carbide Schottky Diode



V _{RRM} =	1200 V
$I_{F(T_{c} = 153^{\circ}C)} =$	20 A
Qc =	65 nC

Features

- Gen4 Thin Chip Technology for Low VF
- Superior Figure of Merit Q[´]_C*V_F
- 100% Avalanche (UIL) Tested
- Enhanced Surge Current Withstand Capability
- Temperature Independent Fast Switching
- Low Thermal Resistance
- Positive Temperature Coefficient of V_F
- High dV/dt Ruggedness



Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

Applications

- Power Factor Correction (PFC)
- Electric Vehicles and Battery Chargers
- Solar Inverters
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- Motor Drives
- Anti-Parallel / Free-Wheeling Diode
- Induction Heating & Welding

Absolute Maximum Ratings (At T_c = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Repetitive Peak Reverse Voltage	V _{RRM}		1200	V	
		T _C = 100°C, D = 1	42		
Continuous Forward Current	IF	T _C = 135°C, D = 1	29	Α	Fig. 4
		T _C = 153°C, D = 1	20		
Non-Repetitive Peak Forward Surge Current, Half Sine	l=	T _C = 25°C, t _P = 10 ms	160	Α	
Wave	IF,SM	T _C = 150°C, t _P = 10 ms	128	А	
Repetitive Peak Forward Surge Current, Half Sine Wave		T _C = 25°C, t _P = 10 ms	96	А	
	IF,RM	T _C = 150°C, t _P = 10 ms	67	A	
Non-Repetitive Peak Forward Surge Current	I _{F,MAX}	T _C = 25°C, t _P = 10 μs	800	Α	
i ² t Value	∫i²dt	T _C = 25°C, t _P = 10 ms	128	A ² s	
Non-Repetitive Avalanche Energy	E _{AS}	L = 0.9 mH, I _{AS} = 20 A	181	mJ	
Diode Ruggedness	dV/dt	V _R = 0 ~ 960 V	200	V/ns	
Power Dissipation	Ртот	T _C = 25°C	266	W	Fig. 3
Operating and Storage Temperature	Tj, Tstg		-55 to 175	°C	



Electrical Characteristics

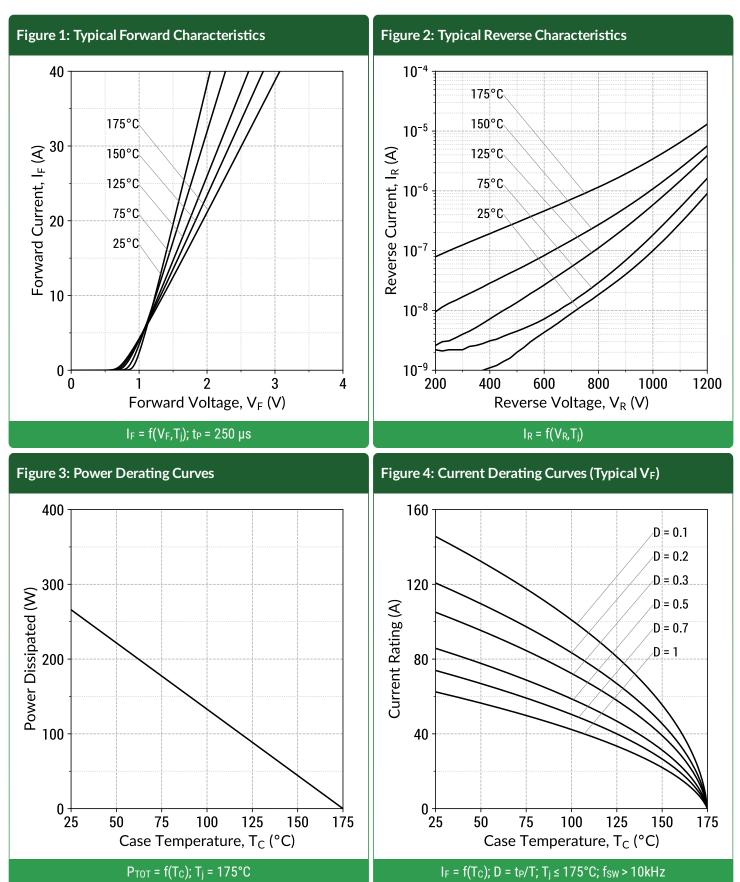
Parameter	Symbol	Conditions		Values			11	Note
Parallieler	Symbol			Min.	Тур.	Max.	Unit	Note
Diode Forward Voltage	VF	I _F = 20 A, T _j = 25°C			1.5	1.8	V	Fig. 1
	VF	I _F = 20 A, T _j = 175°C			1.9			
Reverse Current	la la	V _R = 1200 V,	V _R = 1200 V, T _j = 25°C		1	10		Fig. 2
	IR	V _R = 1200 V, T _j = 175°C			14		μA	
Total Capacitive Charge	0-		V _R = 400 V		45		nC	Fig. 7
	Qc	I _F ≤ I _{F,MAX}	V _R = 800 V		65			
Switching Time	+-	dl _F /dt = 200 A/µs	V _R = 400 V		< 10			
	ts	V _R = 800		< 10			ns	
Tatal Canaditanaa	0	V _R = 1 V, f = 1MHz V _R = 800 V, f = 1MHz			737			Fig. 6
Total Capacitance	С				43	pF		Fig. 6

Thermal/Package Characteristics

Parameter	Symbol	Conditions	Values			l Incia	Note
			Min.	Тур.	Max.	Unit	Note
Thermal Resistance, Junction - Case	RthJC			0.56		°C/W	Fig. 9
Weight	WT			2.0		g	
Mounting Torque	T _M	Screws to Heatsink			1.0	Nm	

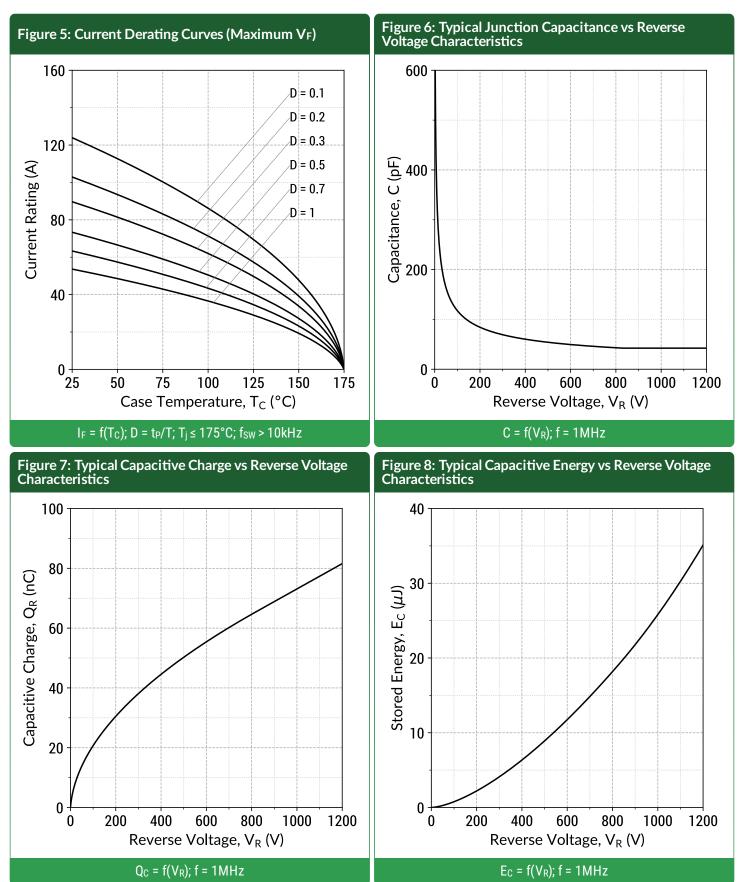






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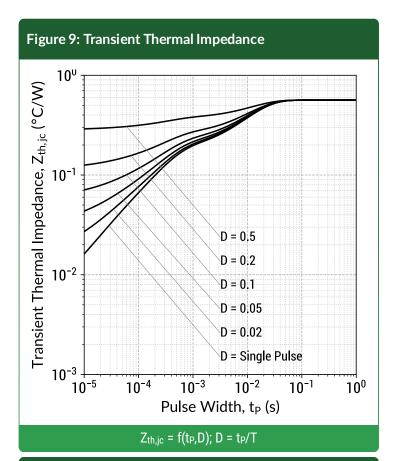


Figure 10: Forward Curve Model

 $I_F = f(V_F, T_j)$

Forward Curve Model Equation:

 $I_{F} = (V_{F} - V_{BI})/R_{DIFF} (A)$

Built-In Voltage (V_{BI}):

 $V_{BI}(T_j) = m \times T_j + n (V)$ m = -0.00119 (V/°C) n = 1.01 (V)

Differential Resistance (RDIFF):

 $R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c (\Omega)$ a = 5.93e-07 (\Omega/°C^2) b = 8.21e-05 (\Omega/°C) c = 0.0244 (\Omega)

Forward Power Loss Equation:

 $P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$

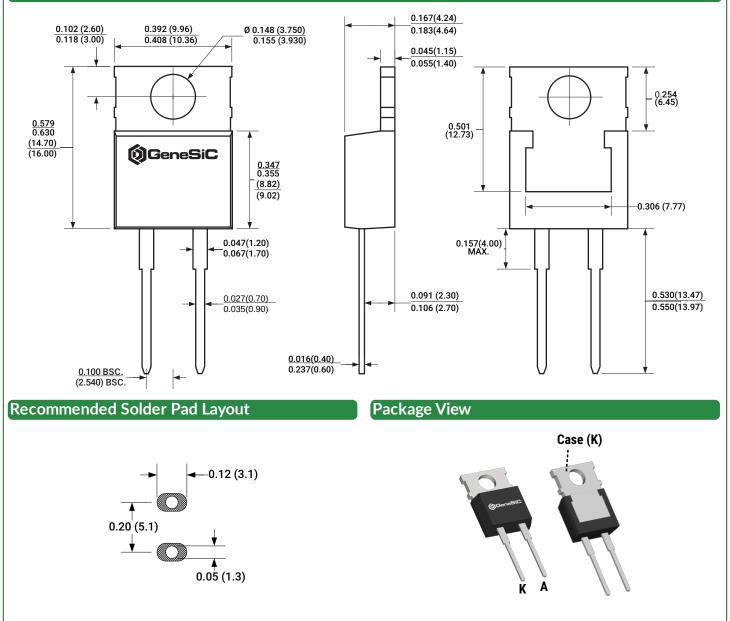


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Package Dimensions

TO-220-2 Package Outline



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.

GD20MPS12A 1200V 20A SiC Schottky MPS™ Diode



Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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Related Links

SPICE Models:	https://www.genesicsemi.com/sic-schottky-mps/GD20MPS12A/GD20MPS12A_SPICE.zip
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- PLECS Models: https://www.genesicsemi.com/sic-schottky-mps/GD20MPS12A/GD20MPS12A_PLECS.zip
- CAD Models: https://www.genesicsemi.com/sic-schottky-mps/GD20MPS12A/GD20MPS12A_3D.zip
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- Reliability: https://www.genesicsemi.com/reliability
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- Quality Manual: https://www.genesicsemi.com/quality

Revision History

Jul. 27, 2020: Initial Release



www.genesicsemi.com/sic-schottky-mps/



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