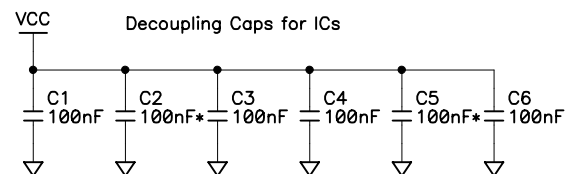


1. ALL RESISTOR VALUES ARE IN OHMS, 1/10W, 5%
2. ALL CAPACITORS ARE 50VDC OR HIGHER.
3. THE ORIGINATION SOURCE OF A VOLTAGE IS REPRESENTED BY (vcc), AND ALL REFERENCES TO THAT VOLTAGE
▲ ARE REPRESENTED BY (vcc).

5. COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL. SEE STUFFING CHART FOR CLARIFICATION.



REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
A	E11217	INITIAL RELEASE, TRACKS A/W @ REV--A	DM	11/20/00	KIS	11/17/01
B	E11564	CHANGED PE7 TO OUTPUT INHIBIT	DM	28JUN01	KIS	7/17/01

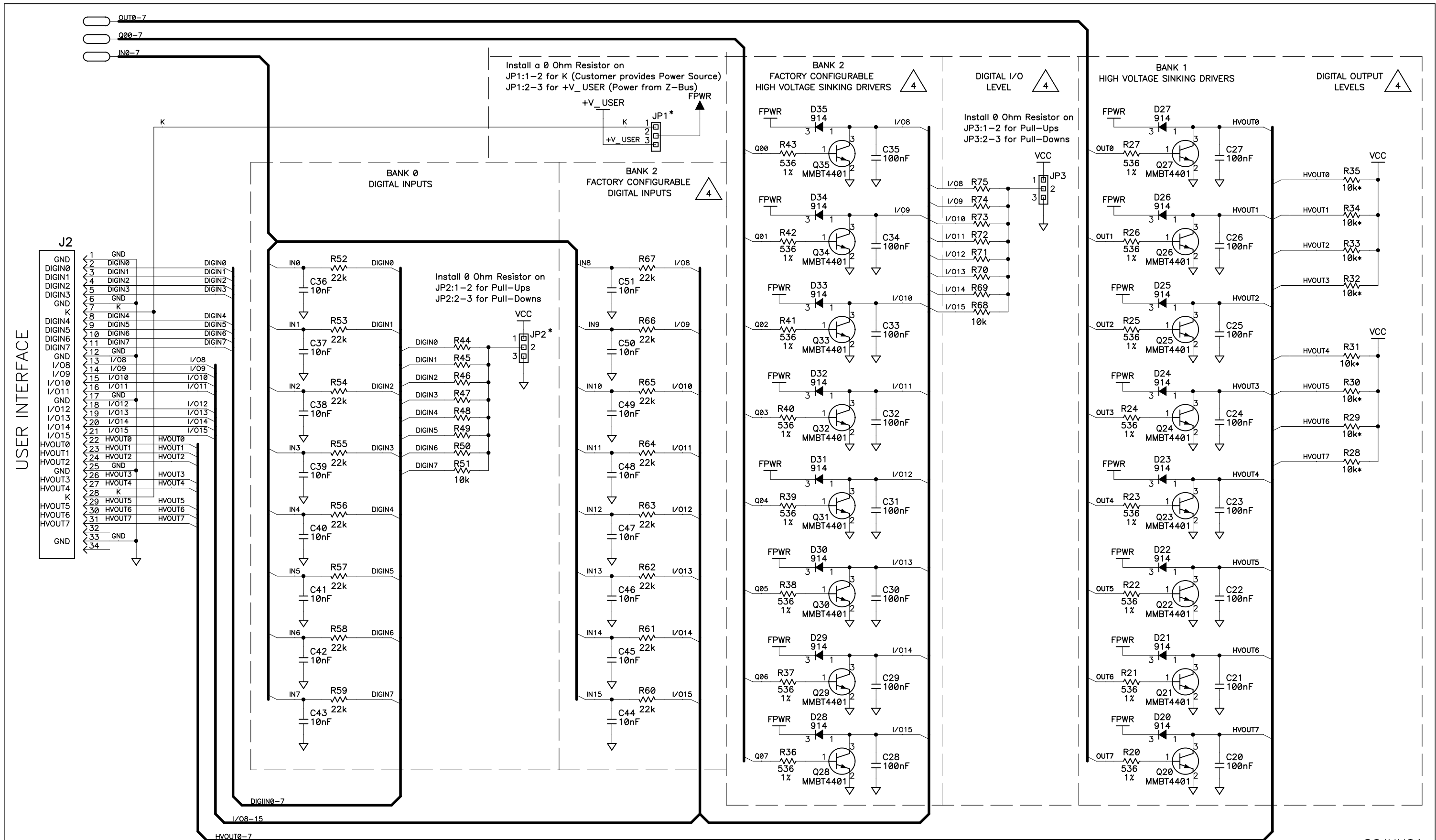
TABLE A

REF DES	DEVICE	DEVICE VOLTAGE INFORMATION					DEVICE: FILTER CAP REF DES(s)
		AGND	GND	VCC	−5V	NO CONNECTS	
U1	74HC257		8	16			C1
U2	74HC257		8	16			C2
U3	74HC138		8	16			C3
U4	74HC08		7	14			C4
U5	74HC273		10	20			C5
U6	74HC273		10	20			C6

STUFFING TABLE

STUFFING TABLE				16 INPUT 8 OUTPUT	8 INPUT 16 OUTPUT	8 INPUT 8 OUTPUT
				MODEL		
CIRCUIT			PART	SR9200	SR9210	SR9220
FPWR SELECT			JP1	ZERO ohm ACROSS PINS 2-3	ZERO ohm ACROSS PINS 2-3	ZERO ohm ACROSS PINS 2-3
BANK 0	INPUT LEVEL SELECT		JP2	ZERO ohm ACROSS PINS 1-2	ZERO ohm ACROSS PINS 1-2	ZERO ohm ACROSS PINS 1-2
BANK 2	HIGH VOLTAGE SINKING DRIVERS	CURRENT DRIVER	R36-43 C28-35 D28-35 Q28-35	NOT INSTALLED	INSTALLED	NOT INSTALLED
		DRIVER CHIP	U5 C5	NOT INSTALLED	INSTALLED	NOT INSTALLED
	DIGITAL INPUTS	INPUT CONDITIONING	R60-67 C44-51	INSTALLED	NOT INSTALLED	NOT INSTALLED
		DRIVER CHIP	U2 C2	INSTALLED	NOT INSTALLED	NOT INSTALLED
	DIGITAL I/O LEVEL		JP3 1-2 R68-75	INSTALLED	NOT INSTALLED	NOT INSTALLED
BANK 1	DIGITAL OUTPUT LEVEL		R28-35	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED

APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE SCHEMATIC DIAGRAM SR9200 SERIES DIGITAL I/O BOARD		 2900 SPAFFORD ST. DAVIS, CA 95616 530 - 757 - 4616	
		DRAWN BY: (INITIAL RELEASE) RM	29JUL99				
		REVISED BY: DM	26JUN01	SIZE B		DWG NO. 090-0101	
		APPROVALS: INITIAL RELEASE					
		PROJECT ENGINEER: Darren Musgrove	7/20/01				
		ENGINEERING MANAGER: R. Matthews	7/20/01				
		SIGNATURES	DATE	SCALE	NONE	RELEASE DATE	SHEET 1 OF 2



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