



PJN1NA60 / PJW1NA60 / PJU1NA60 / PJD1NA60

600V N-Channel MOSFET

Voltage

600 V

Current

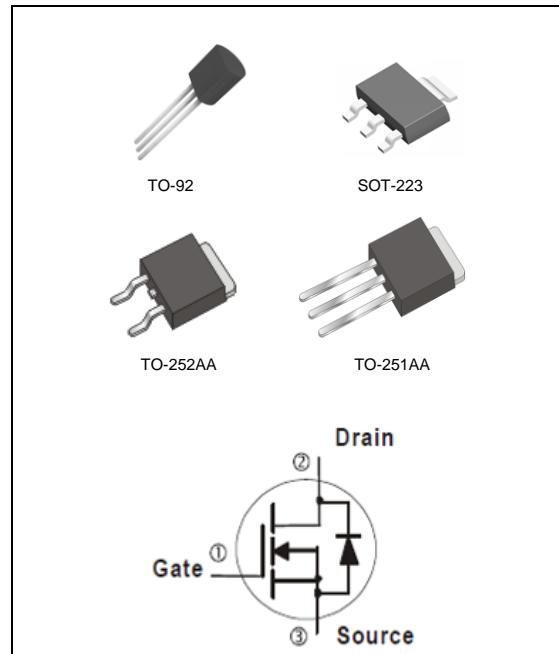
1 A

Features

- $R_{DS(ON)}$, $V_{GS}@10V, I_D@0.5A < 14\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : TO-251AA, TO-252AA, SOT-223, TO-92 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- TO-251AA Approx. Weight : 0.0104 ounces, 0.297grams
- TO-252AA Approx. Weight : 0.0104 ounces, 0.297grams
- SOT-223 Approx. Weight : 0.043 ounces, 0.123grams
- TO-92 Approx. Weight : 0.007 ounces, 0.196grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	TO-251AA	TO-252AA	SOT-223	TO-92	UNITS
Drain-Source Voltage		V_{DS}	600				V
Gate-Source Voltage		V_{GS}	±30				V
Continuous Drain Current		I_D	1		0.3		A
Pulsed Drain Current		I_{DM}	4		1.2		A
Single Pulse Avalanche Energy ^(Note 1)		E_{AS}	50				mJ
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	27		3.3	3	W
	Derate above 25°C		0.216		0.026	0.024	W/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150				$^\circ\text{C}$
Typical Thermal resistance							
- Junction to Case		$R_{\theta JC}$	4.63		-	-	$^\circ\text{C/W}$
- Junction to Ambient		$R_{\theta JA}$	110		37.9 ^(Note 4)	140	

- Limited only By Maximum Junction Temperature



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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	600	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3.34	4	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=0.5A$	-	11.1	14	Ω
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=600V, V_{GS}=0V$	-	0.02	1.0	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V$	-	± 10	± 100	nA
Diode Forward Voltage	V_{SD}	$I_S=1A, V_{GS}=0V$	-	0.85	1.4	V
Dynamic (Note 5)						
Total Gate Charge	Q_g	$V_{DS}=480V, I_D=1A,$ $V_{GS}=10V$ (Note 2,3)	-	3.3	-	nC
Gate-Source Charge	Q_{gs}		-	1.1	-	
Gate-Drain Charge	Q_{gd}		-	1	-	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0\text{MHz}$	-	95	-	pF
Output Capacitance	C_{oss}		-	21	-	
Reverse Transfer Capacitance	C_{rss}		-	0.3	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=300V, I_D=1A,$ $R_G=25\Omega$ (Note 2,3)	-	5	-	ns
Turn-On Rise Time	t_r		-	20	-	
Turn-Off Delay Time	$t_{d(off)}$		-	8	-	
Turn-Off Fall Time	t_f		-	25	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	1	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}	---	-	-	4	A
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_S=1A$	-	190	-	ns
Reverse Recovery Charge	Q_{rr}	$di_f/dt=100A/\mu s$ (Note 2)	-	0.53	-	μC

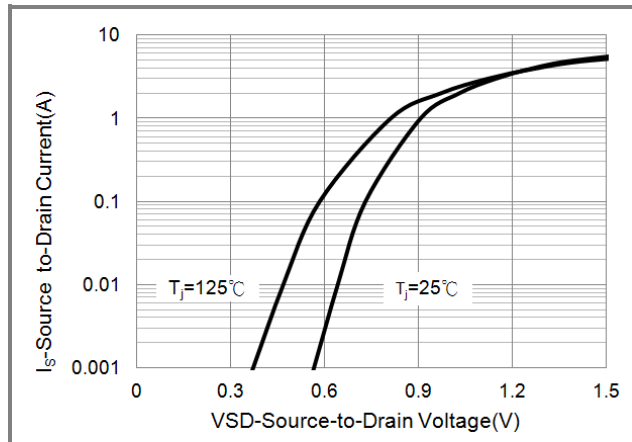
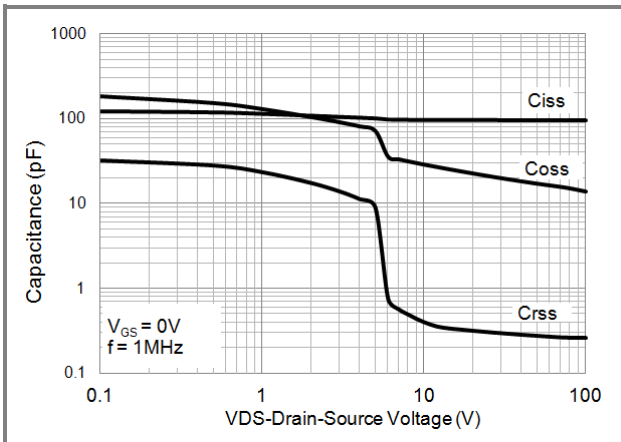
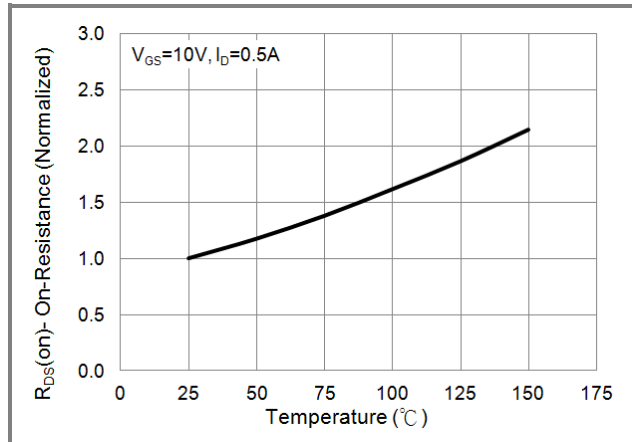
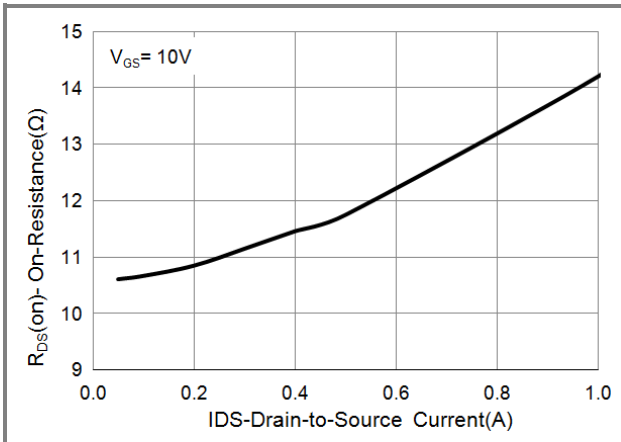
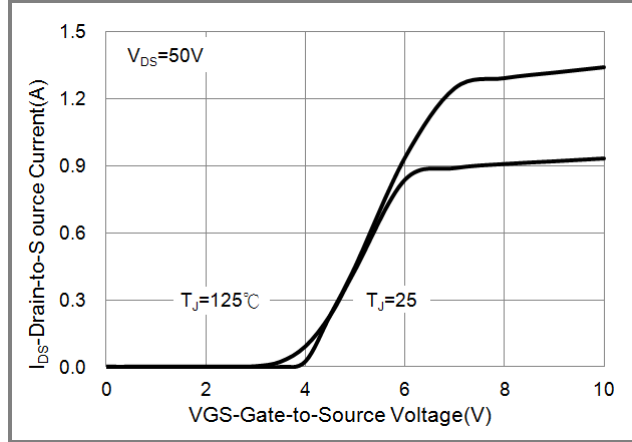
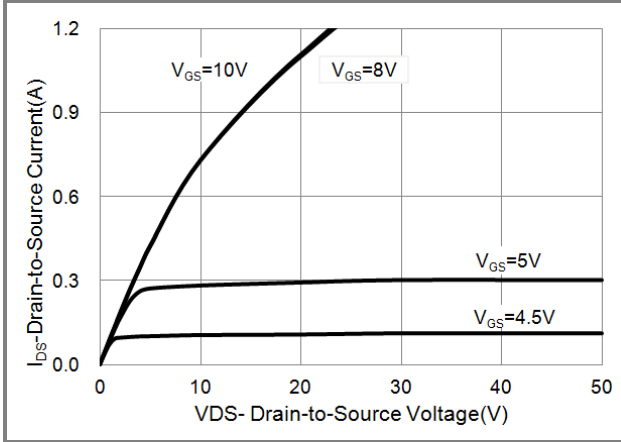
NOTES :

1. $L=30\text{mH}, I_{AS}=1.77A, V_{DD}=50V, R_G=25\text{ohm}$, Starting $T_J=25^\circ\text{C}$
2. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
3. Essentially independent of operating temperature typical characteristics.
4. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
5. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES





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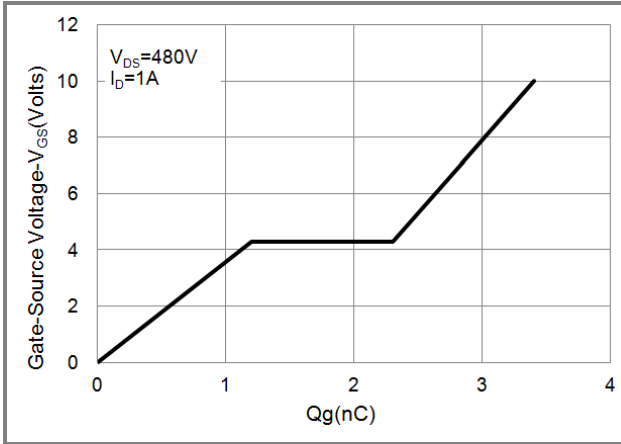


Fig.7 Gate Charge

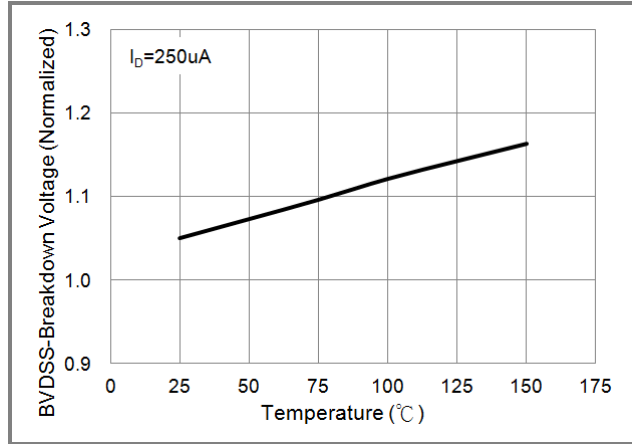


Fig.8 $B_{V_{DS}}$ vs. Junction Temperature

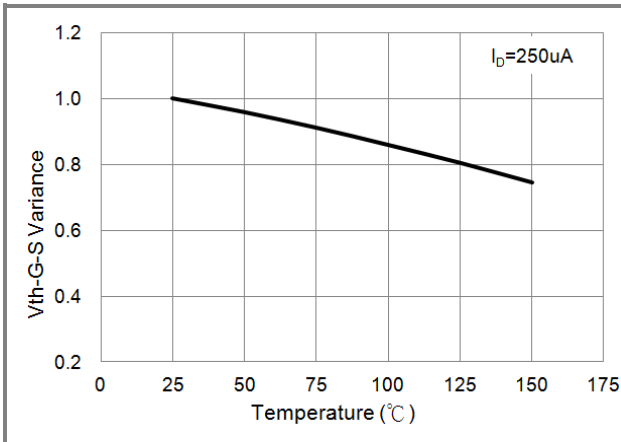


Fig.9 Threshold Voltage Variation with Temperature

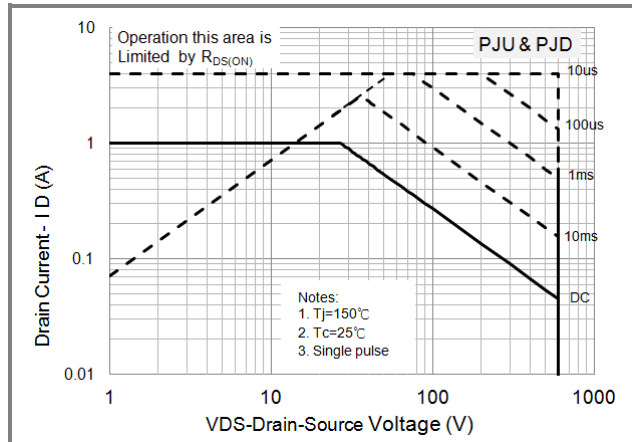


Fig.10 Maximum Safe Operating Area

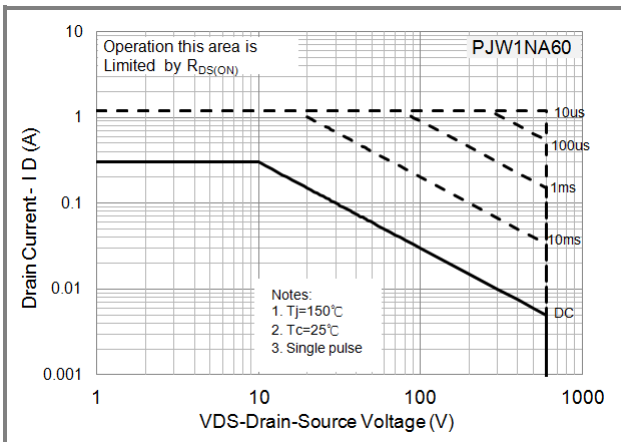


Fig.11 Maximum Safe Operating Area

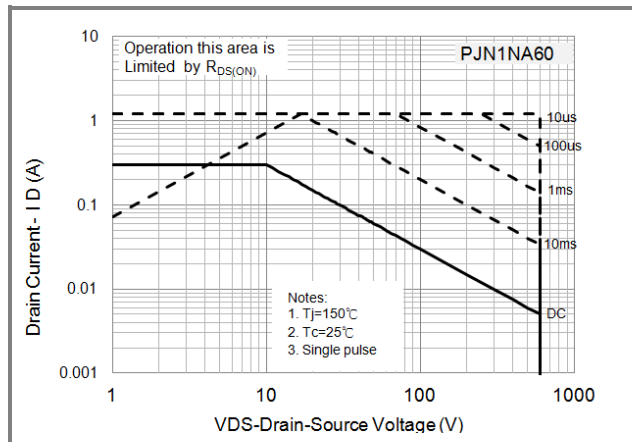


Fig.12 Maximum Safe Operating Area



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TYPICAL CHARACTERISTIC CURVES

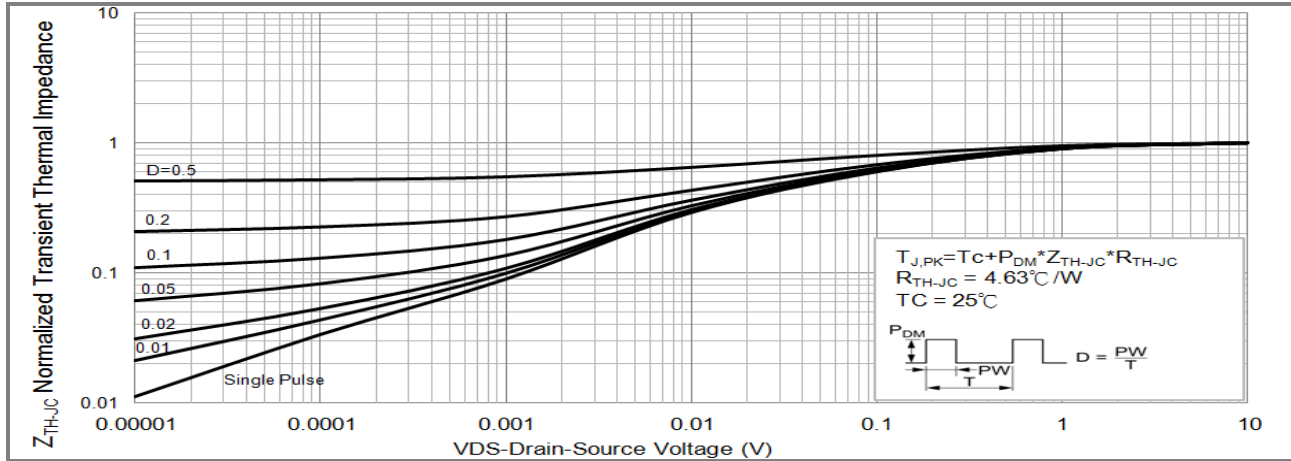


Fig.13 PJU/PJD Normalized Transient Thermal Impedance vs. Pulse Width

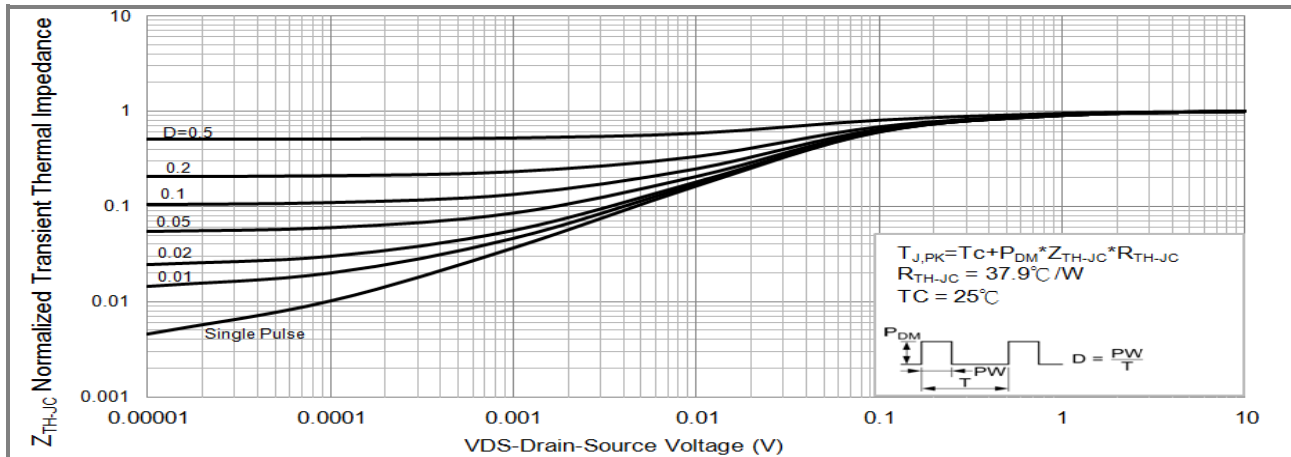


Fig.14 PJW1NA60 Normalized Transient Thermal Impedance vs. Pulse Width

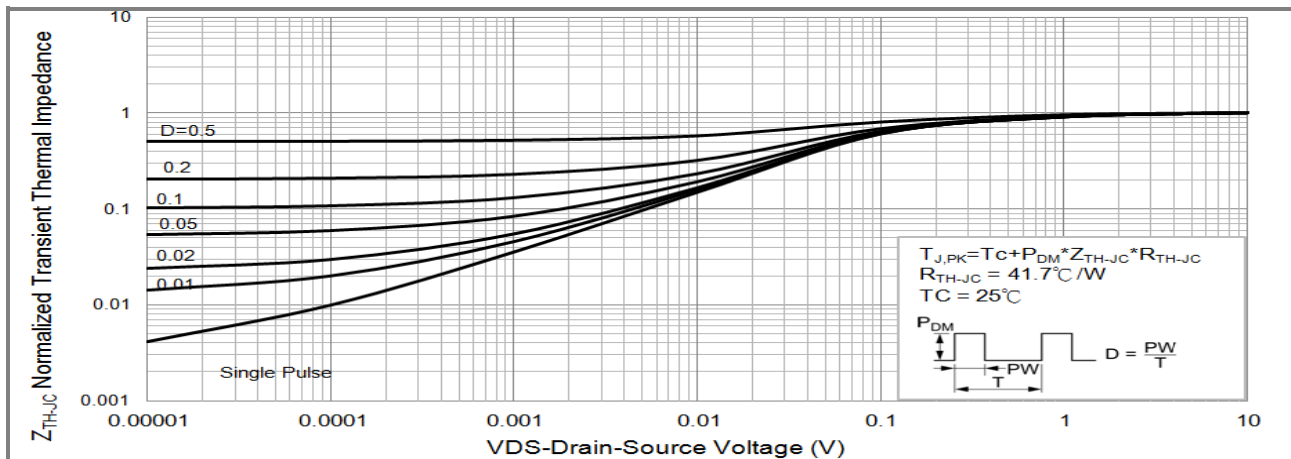
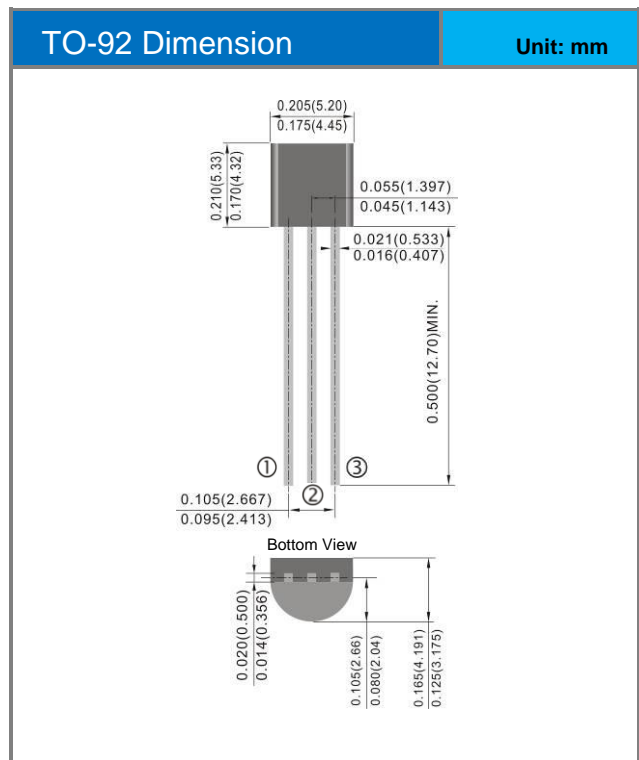
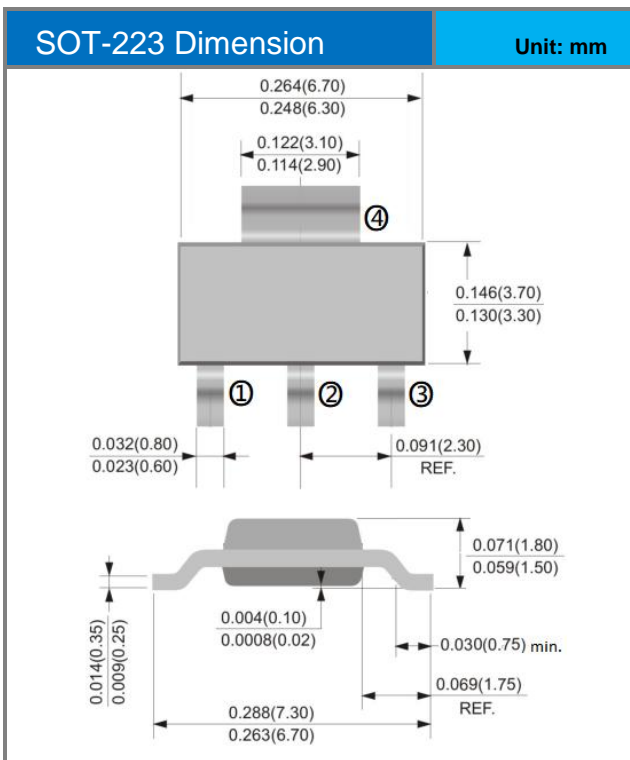
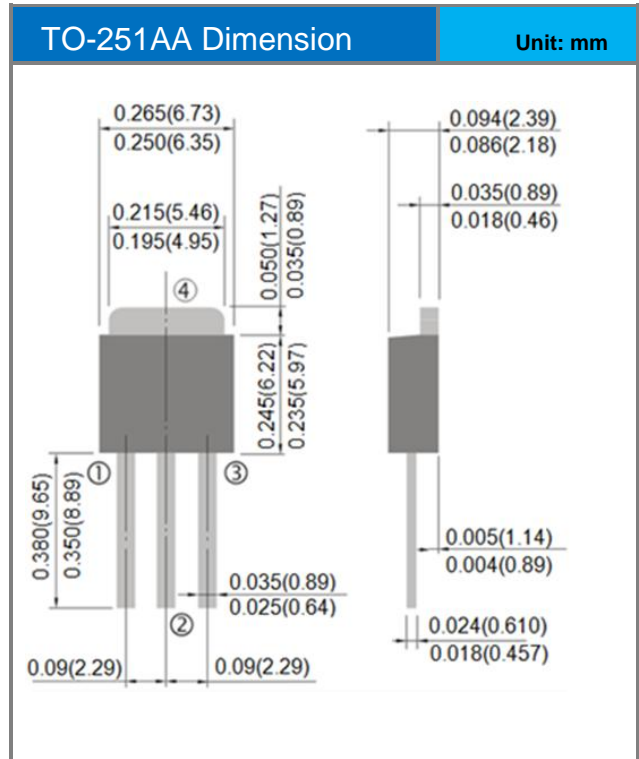
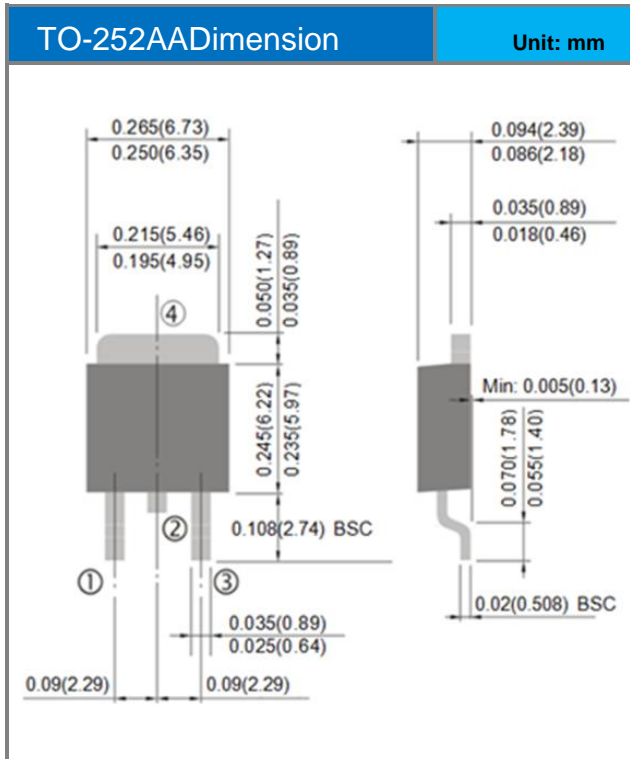


Fig.15 PJN1NA60 Normalized Transient Thermal Impedance vs. Pulse Width



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Packaging Information





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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJU1NA60_T0_00001	TO-251AA	80pcs / Tube	U1NA60	Halogen free
PJD1NA60_L2_00001	TO-252AA	3,000pcs / 13" reel	D1NA60	Halogen free
PJW1NA60_R2_00001	SOT-223	2,500pcs / 13" reel	1NA60	Halogen free
PJN1NA60_B0_00001	TO-92	1000pcs / bag	1NA60	Halogen free
PJN1NA60_A0_00001	TO-92 AMMO	2000pcs / box	1NA60	Halogen free



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