	- 1 - 1
ΡΛΝ	JIT
	SEMI
	CONDUCTOR

60V P-Channel Enhancement Mode MOSFET

-60 V Current

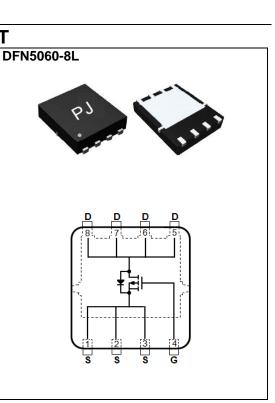
Features

Voltage

- $R_{DS(ON)}$, V_{GS} @-10V, I_D @-8A<48m Ω
- $R_{DS(ON)}$, V_{GS} @-4.5V, I_D @-4A<65m Ω
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 Standard

Mechanical Data

- Case : DFN5060-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0028 ounces, 0.08 gram



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

-16 A

PARAMETE	R	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage Gate-Source Voltage		V _{DS}	-60	V	
		V _{GS}	<u>+</u> 20		
Continuous Drain Current (Note 4)	T _C =25°C		-16		
	T _c =100°C	I _D	-10	А	
Pulsed Drain Current (Note 1)	T _C =25°C	I _{DM}	-64		
Power Dissipation	T _C =25°C		25	14/	
	T _c =100°C	PD	10	W	
Continuous Drain Current (Note 4)	T _A =25°C		-5	•	
	T _A =70°C	I _D	-4	A	
Power Dissipation	T _A =25°C		2		
	T _A =70°C	PD	1.3	W	
Single Pulse Avalanche Energy (Note 6)		E _{AS}	51	mJ	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance (Note 4,5)	Junction to Case	R _{θJC}	5	90.444	
	Junction to Ambient	R _{θJA}	62.5	°C/W	



Electrical Characteristics (T_A=25°C unless otherwise noted)

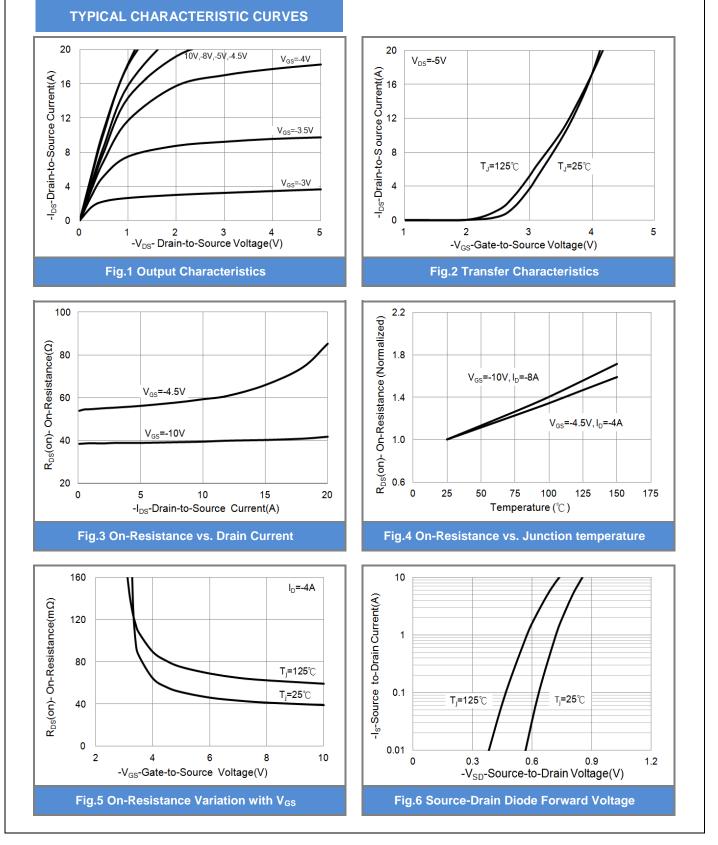
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-60	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-1	-1.7	-2.5	
Drain-Source On-State Resistance	D	V _{GS} =-10V, I _D =-8A	-	40	48	mΩ
	R _{DS(on)}	V _{GS} =-4.5V, I _D =-4A	-	55	65	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Qg	V _{DS} =-30V, I _D =-8A, V _{GS} =-10V ^(Note 3)	-	22	-	nC
Gate-Source Charge	Q _{gs}		-	4.1	-	
Gate-Drain Charge	Q _{gd}		-	5.2	-	
Input Capacitance	Ciss	V _{DS} =-30V, V _{GS} =0V,	-	1256	-	pF
Output Capacitance	Coss		-	87	-	
Reverse Transfer Capacitance	Crss	f=1MHZ	-	59	-	
Turn-On Delay Time	td _(on)	V_{DD} =-30V, I _D =-1A, V_{GS} =-10V, R _G =6Ω (Note 3)	-	13	-	
Turn-On Rise Time	tr		-	42	-	
Turn-Off Delay Time	td _(off)		-	65	-	ns
Turn-Off Fall Time	t _f		-	16	-	
Drain-Source Diode						
Maximum Continuous Drain-Source		I _S			16	
Diode Forward Current	IS		-	-	-16	A
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V	-	-0.72	-1	V

NOTES :

1. Pulse width

- 2. Essentially independent of operating temperature typical characteristics.
- Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited.
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH, I_{AS} =32A, V_{DD} =25V, V_{GS} =10V.
- 7. Guaranteed by design, not subject to production testing.

April 01,2019-REV.00

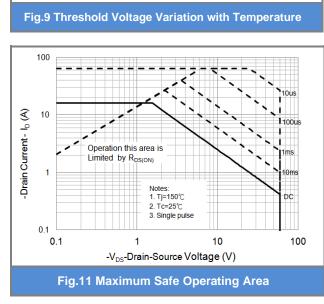


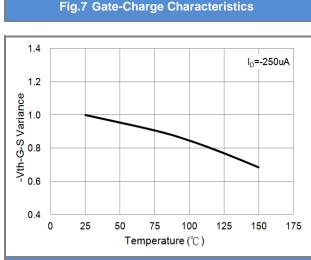
PJQ5465A-AU

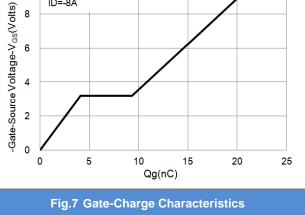


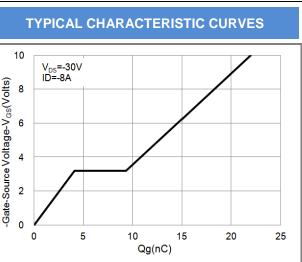
April 01,2019-REV.00

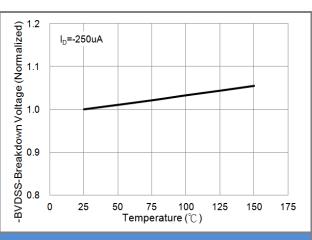














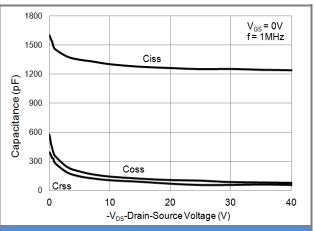
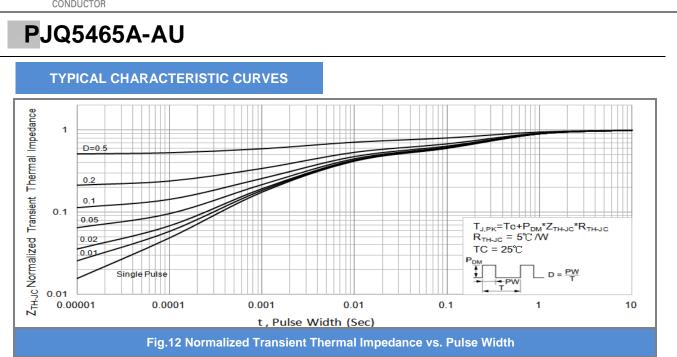


Fig.10 Capacitance vs. Drain-Source Voltage



SEMI

PAN



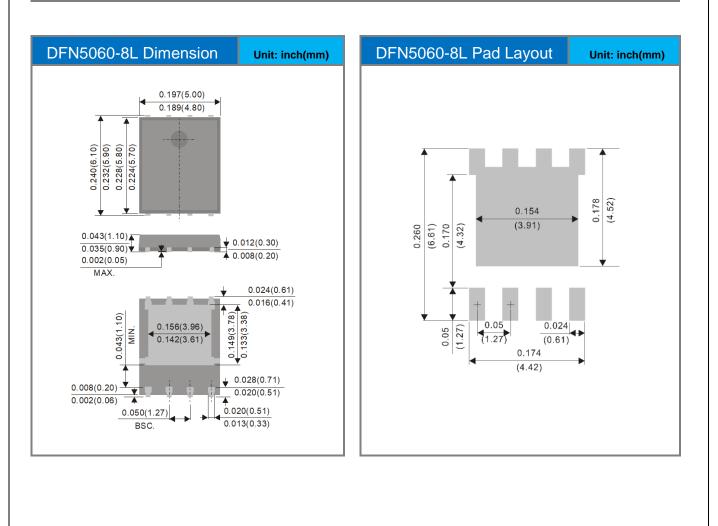




Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJQ5465A-AU_R2_000A1	DFN5060-8L	3000pcs / 13" reel	Q5465A	Halogen free

Packaging Information & Mounting Pad Layout





Disclaimer

- Reproducing and modifying information of the document is prohibited without permission from Panjit International Inc..
- Panjit International Inc. reserves the rights to make changes of the content herein the document anytime without notification. Please refer to our website for the latest document.
- Panjit International Inc. disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- Panjit International Inc. does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Panjit International Inc. makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- The products shown herein are not designed and authorized for equipments relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panjit International Inc. for any damages resulting from such improper use or sale.
- Since Panjit uses lot number as the tracking base, please provide the lot number for tracking when complaining.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Panjit: <u>PJQ5465A-AU_R2_000A1</u>