

## CY8C4125AZ5-M443T



Status: In Production

Product Specs	Pricing & Inventory	Packaging/Ordering	Quality and RoHS	Technical Documents
<b>CY8C4125AZ5-M443T</b>				<b>DESIGN SUPPORT</b>
Automotive Qualified	Y			Development Kits
CPU Core	ARM Cortex-M0			Forums
CapSense	Y			Knowledge Base
Dedicated ADC (#Max Resolution @ Sample Rate)	SAR (1, 12-bit @ 806 ksps)			Packaging
EEPROM (KB)	0			Product Brochures
Flash (KB)	32			Quality & Reliability
JTAG and Si ID	0x11C0			Samples
LCD Direct Drive	Y			Technical Support
Max. Operating Frequency (MHz)	24			Technical Training
Max. Operating Temp. (°C)	105			Third Party Network (CYPros)
Max. Operating Voltage (V)	5.50			
Min. Operating Temp. (°C)	-40			
Min. Operating Voltage (V)	1.71			
No. of CAN Controllers	0			
No. of Continuous Time Blocks	1			
No. of DMA Channels	8			
No. of Dedicated Comparators	2			
No. of Dedicated Timer/Counter/PWM Blocks	8			
No. of GPIOs	38			
No. of Op Amps	2			
No. of SIO	0			
No. of Serial Communication Blocks	4			
No. of USB IO	0			
No. of Universal Analog Blocks	0			
No. of Universal Digital Blocks	0			
PLL	N			
SRAM (KB)	4			
Series	PSoC 4100M			
Tape & Reel	Y			
USB (Type)	None			

### PRICING & INVENTORY AVAILABILITY

1-9 unit Price*	10-24 unit Price*	25-99 unit Price*	100-249 unit Price*	250-999 unit Price*	1000+ unit Price*
<a href="#">Contact Sales</a>					

Availability	Quantity	Ships In	Buy from Cypress	Buy from Distributors
Out of Stock	0	<a href="#">Please click here to check lead times</a>	<a href="#">Contact Sales</a>	<a href="#">Contact Sales</a>

\* Prices are shown in United States dollars and are for budgetary use only for volume of 1ku

### PACKAGING/ORDERING

Package	TQFP
No. of Pins	44
Package Dimensions	275 L x 1.4 H x 275 W (Mils)
Package Cross Section Drawing	<a href="#">Download</a>
Package Carrier	REEL
Standard Pack Quantity	1500
Minimum Order Quantity (MOQ)	1500
Order Increment	1500
Estimated Lead Time (days)	91
HTS Code	8542.31.0001
ECCN	(A.3)
ECCN Suball	3A991.A.3

### QUALITY AND ROHS

Moisture Sensitivity Level (MSL)	3
Peak Reflow Temp. (°C)	260 (Cypress Reflow Profile)
RoHS Compliant	Y Print RoHS Certificate of Compliance
PB Free	Y
Lead/Ball Finish	Ni/Pd/Au
Marking	Cypress Marking Format

### Device Qualification Reports

FIT/MTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report.  
[QTPM162707: AUTOMOTIVE PSoC 4100M and PSoC 4200M DEVICE FAMILY S8PFHD-10P, FAB25 \(AL BEOL INTERCONNECT\).pdf](#) Last Update: Mar 06, 2019

### TECHNICAL DOCUMENTS

#### Application Notes (17)

- [AN84858 - PSoC®4 Programming Using an External Microcontroller \(HSSP\) Jan 19, 2020](#)
  - [AN93637 - PSoC® 4 Sensorless Field-Oriented Control \(FOC\) Dec 30, 2019](#)
  - [AN82156 - PSoC® 3, PSoC 4, and PSoC 5LP - Designing PSoC Creator™ Components With UDB Datapaths Oct 02, 2019](#)
  - [AN82250 - PSoC Creator - Implementing Programmable Logic Designs with Verilog Sep 22, 2019](#)
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#### DESIGN SUPPORT

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Product Roadmaps

#### ABOUT US

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#### CORPORATE HEADQUARTERS

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#### CUSTOMER SERVICE SUPPORT

+1-800-541-4736  
Hours:  
8:00AM - 5:00PM (local time)  
Create a MyCase  
Cypress Developer Community

#### INTERNATIONAL SUPPORT

+1-408-943-2600  
United States +1-800-541-4736  
Hours:  
4:30AM - 1:30PM (pacific time)  
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