

CR08AS-12A

600V - 0.8A - Thyristor Low Power Use

R07DS0489EJ0300 Rev.3.00 May 22, 2013

Features

$$\begin{split} \bullet & \quad I_{T \, (AV)} : 0.8 \; A \\ \bullet & \quad V_{DRM} : 600 \; V \\ \bullet & \quad I_{GT} : 100 \; \mu A \end{split}$$

- Non-Insulated Type
- Planar Type
- Surface Mounted type

Outline

RENESAS Package code: PLZZ0004CA-A (Package name: UPAK)





- 1. Cathode
- 2. Anode
- 3. Gate
- 4. Anode

Applications

Solid state relay, strobe flasher, igniter, and hybrid IC

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
Farameter	Symbol	12	Onit	
Repetitive peak reverse voltage	V_{RRM}	600	V	
Non-repetitive peak reverse voltage	V_{RSM}	720	V	
DC reverse voltage	$V_{R(DC)}$	480	V	
Repetitive peak off-state voltage Note1	V_{DRM}	600	V	
DC off-state voltage Note1	$V_{D(DC)}$	480	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T(RMS)}	1.26	Α	
Average on-state current	I _{T(AV)}	0.8	А	Commercial frequency, sine half wave 180° conduction, Ta=51°C Note2
Surge on-state current	I _{TSM}	10	A	60Hz sine half wave, 1full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.42	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	0.5	W	
Average gate power dissipation	$P_{G(AV)}$	0.1	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.3	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	50	mg	Typical value

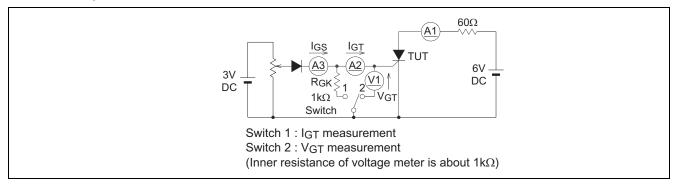
Notes: 1. With gate to cathode resistance R_{GK} = 1 $k\Omega$

Electrical Characteristics

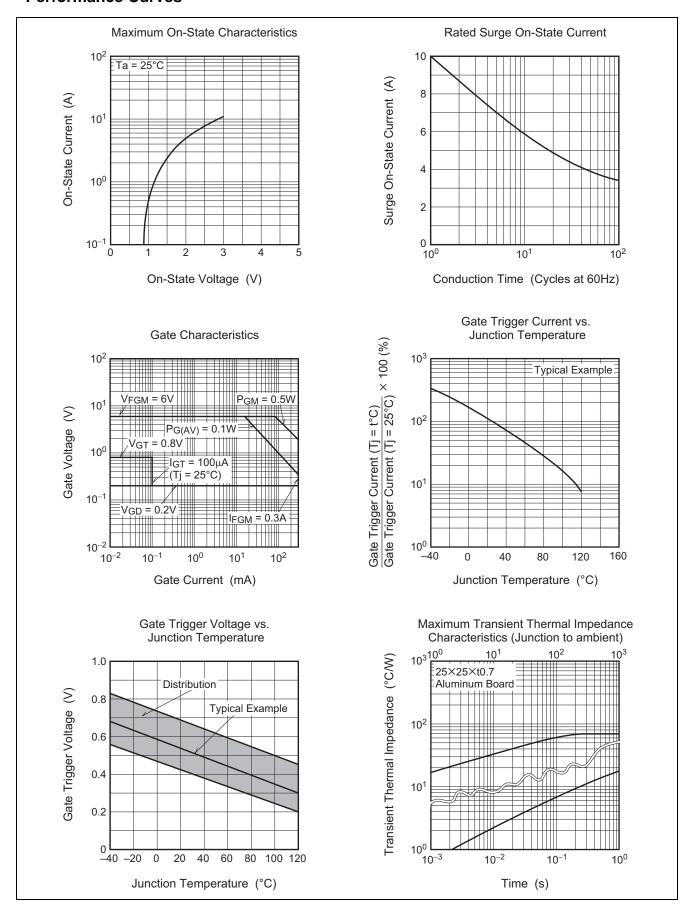
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I _{RRM}	_	_	0.5	mA	$Tj = 125$ °C, V_{RRM} applied $R_{GK} = 1 \text{ k}\Omega$
Repetitive peak off-state current	I _{DRM}	1	_	0.5	mA	$Tj = 125$ °C, V_{DRM} applied $R_{GK} = 1 \text{ k}\Omega$
On-state voltage	V_{TM}	1	_	1.5	٧	$Tj = 25$ °C, $I_{TM} = 2.5$ A instantaneous value
Gate trigger voltage	V _{GT}	_	_	0.8	V	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A Note3
Gate non-trigger voltage	V_{GD}	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$ $R_{GK} = 1 k\Omega$
Gate trigger current	I _{GT}	1	_	100	μΑ	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A Note3
Holding current	I _H		1.5	3	mA	$Tj = 25$ °C, $V_D = 12$ V $R_{GK} = 1$ k Ω
Thermal resistance	R _{th(j-a)}	_	_	65	°C/W	Junction to ambient Note2

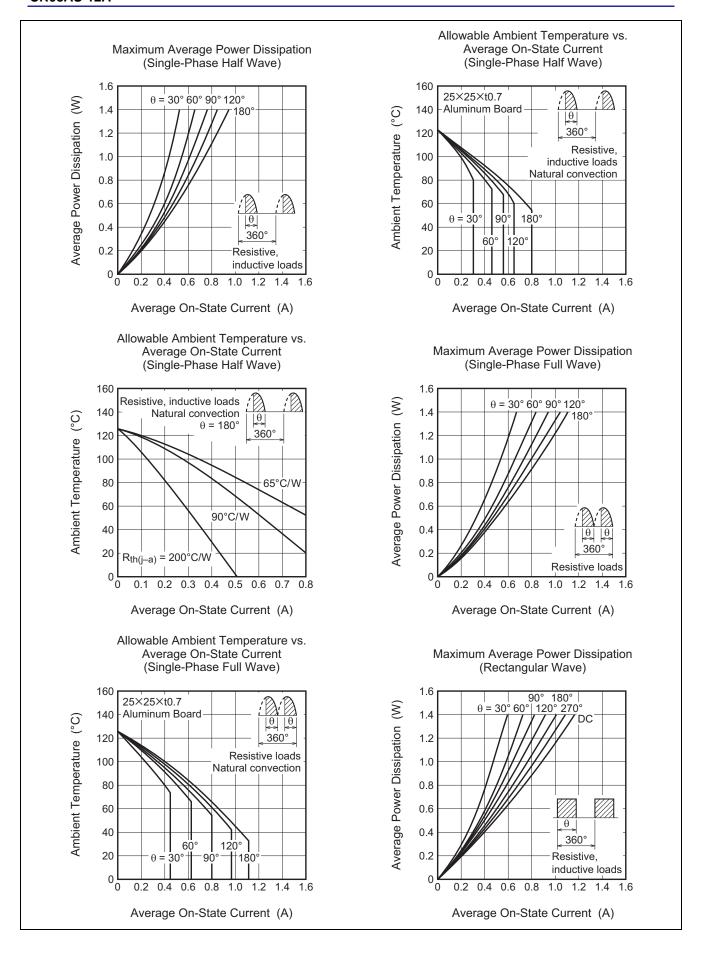
Notes: 2. Soldering with ceramic plate (25 mm \times 25 mm \times t0.7 mm).

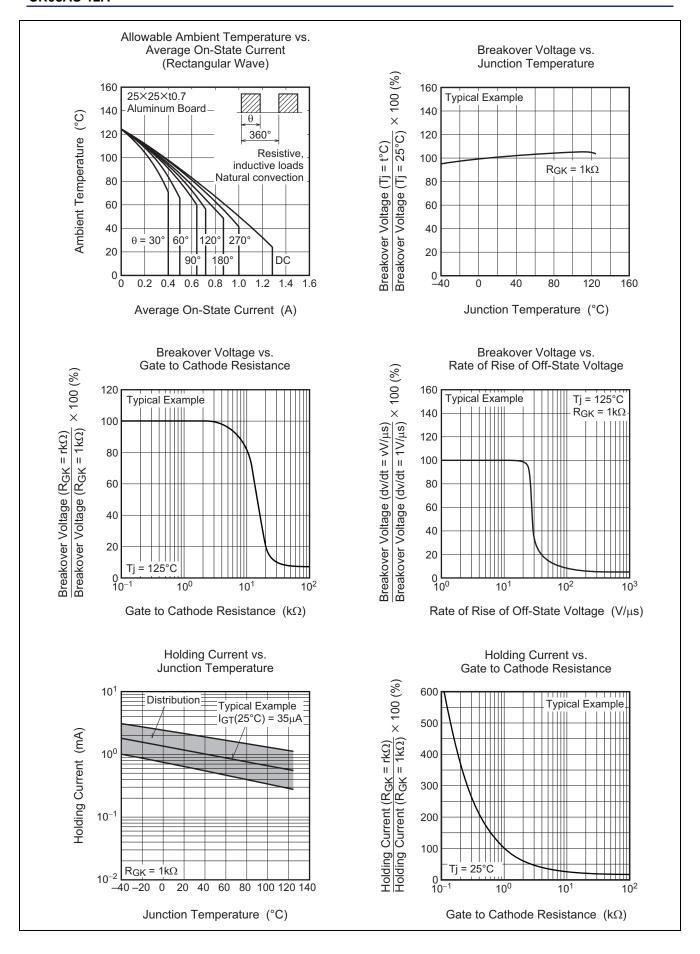
3. Igt, Vgt measurement circuit.

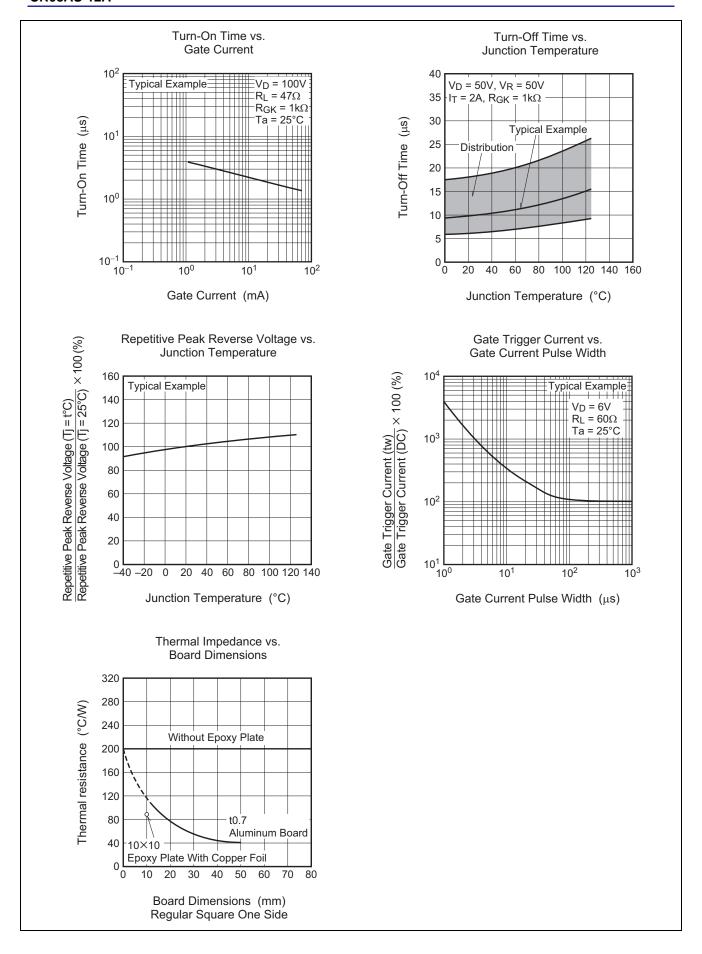


Performance Curves

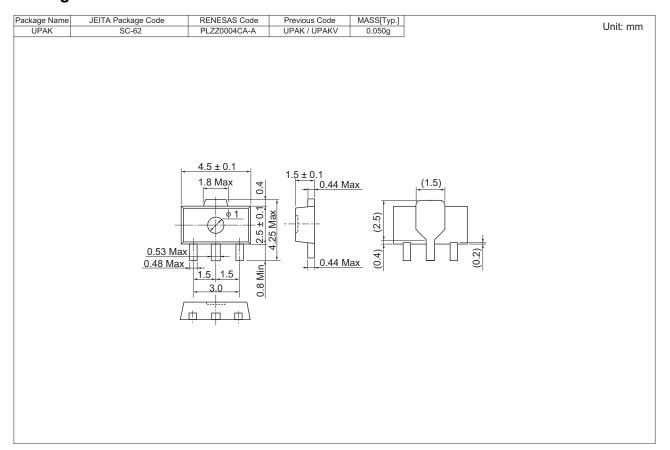








Package Dimensions



Ordering Information

Orderable Part Number	Packing	Quantity	Remark
CR08AS-12A-T14 #B10	Embossed Tape	4000 pcs.	Taping direction "T1"

Note: Please confirm the specification about the shipping in detail.

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