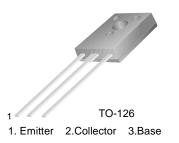
FAIRCHILD

SEMICONDUCTOR®

KSE210

Feature

- Low Collector-Emitter Saturation Voltage
- High Current Gain Bandwidth Product : f_T=65MHz@I_C= -100mA (Min.)
- Complement to KSE200



PNP Epitaxial Silicon Transistor

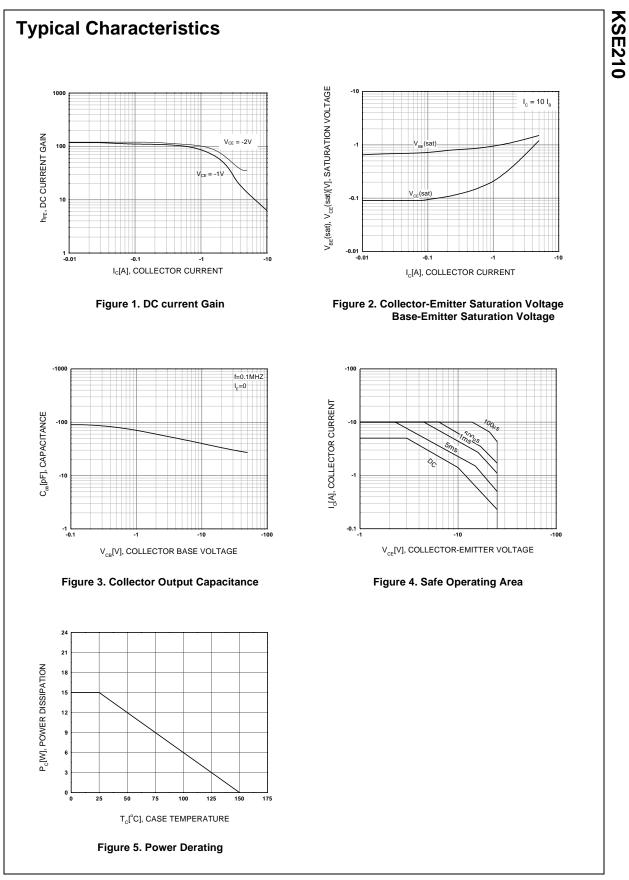
A	bsol	ute	Maxi	imum	Rating	S	T _C =25°C unless otherwise noted
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Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	- 40	V
V _{CEO}	Collector-Emitter Voltage	- 25	V
V _{EBO}	Emitter-Base Voltage	- 8	V
I _C	Collector Current	- 5	A
P _C	Collector Dissipation (T _C =25°C)	15	W
T _J Junction Temperature		150	°C
T _{STG} Storage Temperature		- 65 ~ 150	°C

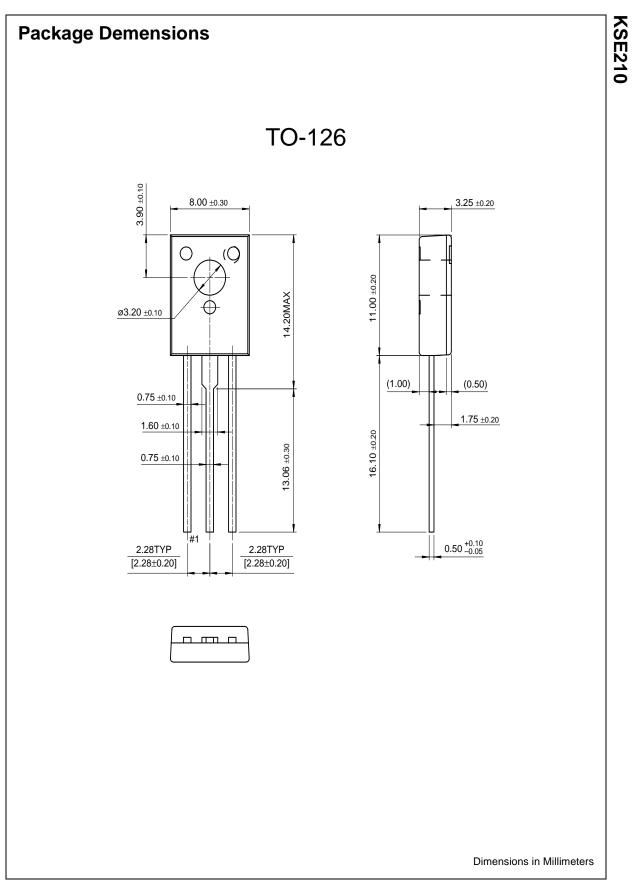
Electrical Characteristics $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = - 10mA, I _B = 0	-25		V
I _{CBO}	Collector Cut-off Current	$V_{CB} = -40V, I_{E} = 0$		-100	nA
		$V_{CB} = -40V, I_E = 0 @ T_J = 125^{\circ}C$		-100	μA
I _{EBO}	Emitter Cut-off Current	$V_{BE} = -8V, I_{C} = 0$		-100	nA
h _{FE1}	DC Current Gain	V _{CE} = - 1V, I _C = - 500mA	70		
h _{FE2}		V _{CE} = - 1V, I _C = - 2A	45	180	
h _{FE3}		V _{CE} = - 2V, I _C = - 5A	10		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = - 500mA, I _B = - 50mA		-0.3	V
		I _C = - 2A, I _C = - 200mA		-0.75	V
		I _C = - 5A, I _B = - 1A		-1.8	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = - 5A, I _B = - 1A		-2.5	V
V _{BE} (on)	Base-Emitter On Voltage	V _{CE} = - 1V, I _C = - 2A		-1.6	V
f _T	Current Gain Bandwidth Product	V _{CE} = - 10V, I _C = - 100mA	65		MHz
C _{ob}	Output Capacitance	V _{CB} = - 10V, I _E = 0, f = 1MHz		120	pF

KSE210



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EcoSPARK™ E ² CMOS™	ISOPLANAR™ LittleFET™	QT Optoelectronics™ Quiet Series™	UltraFET [®] VCX™
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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.	
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